

FIG. 1A

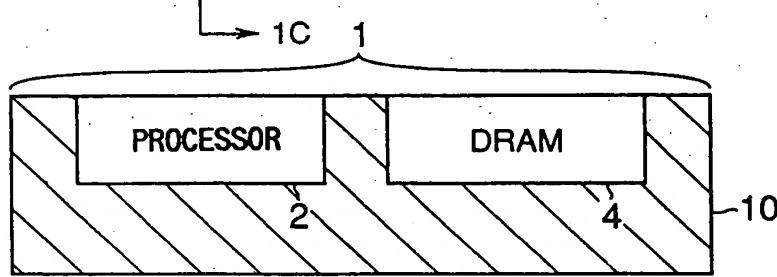


FIG. 1B

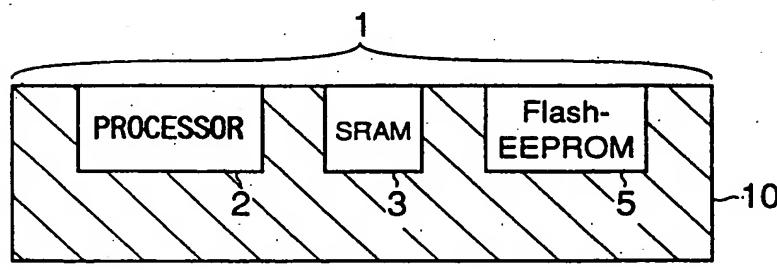


FIG. 1C

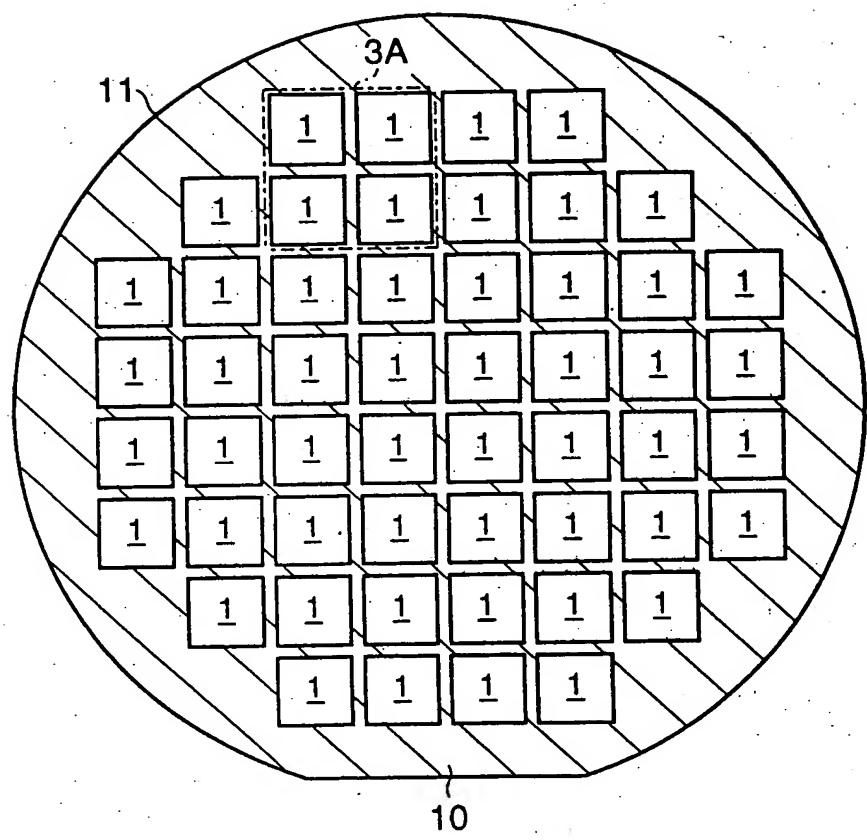
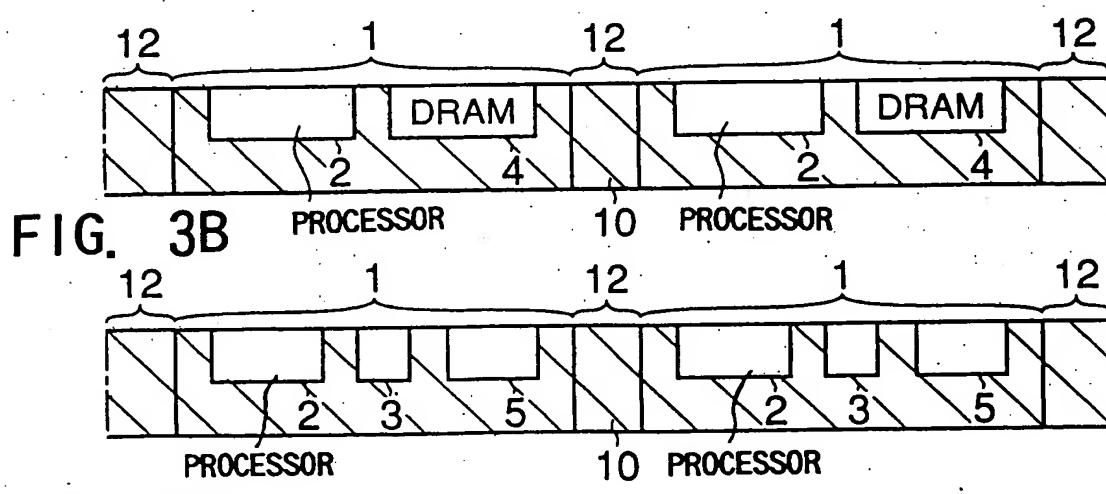
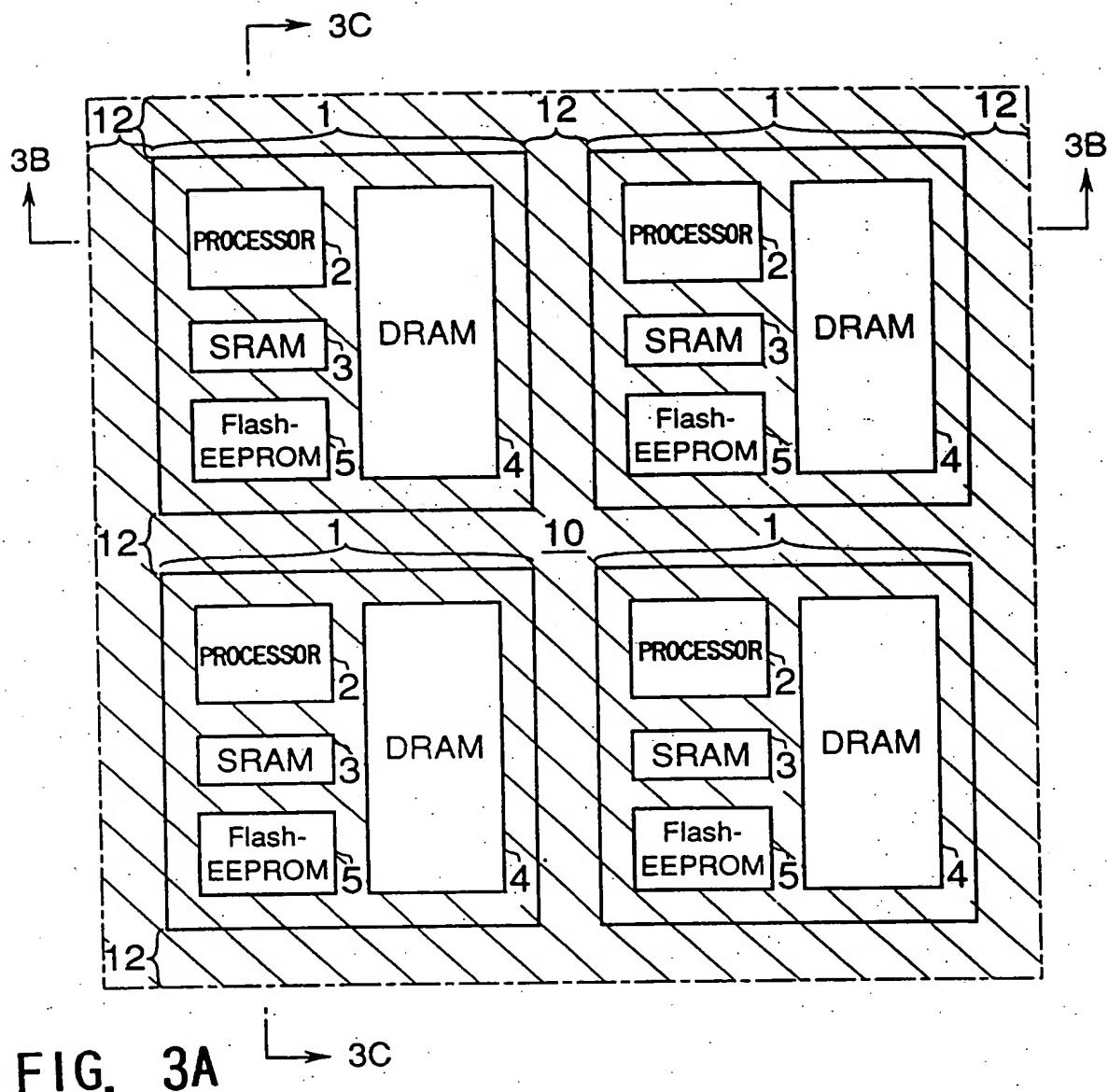


FIG. 2



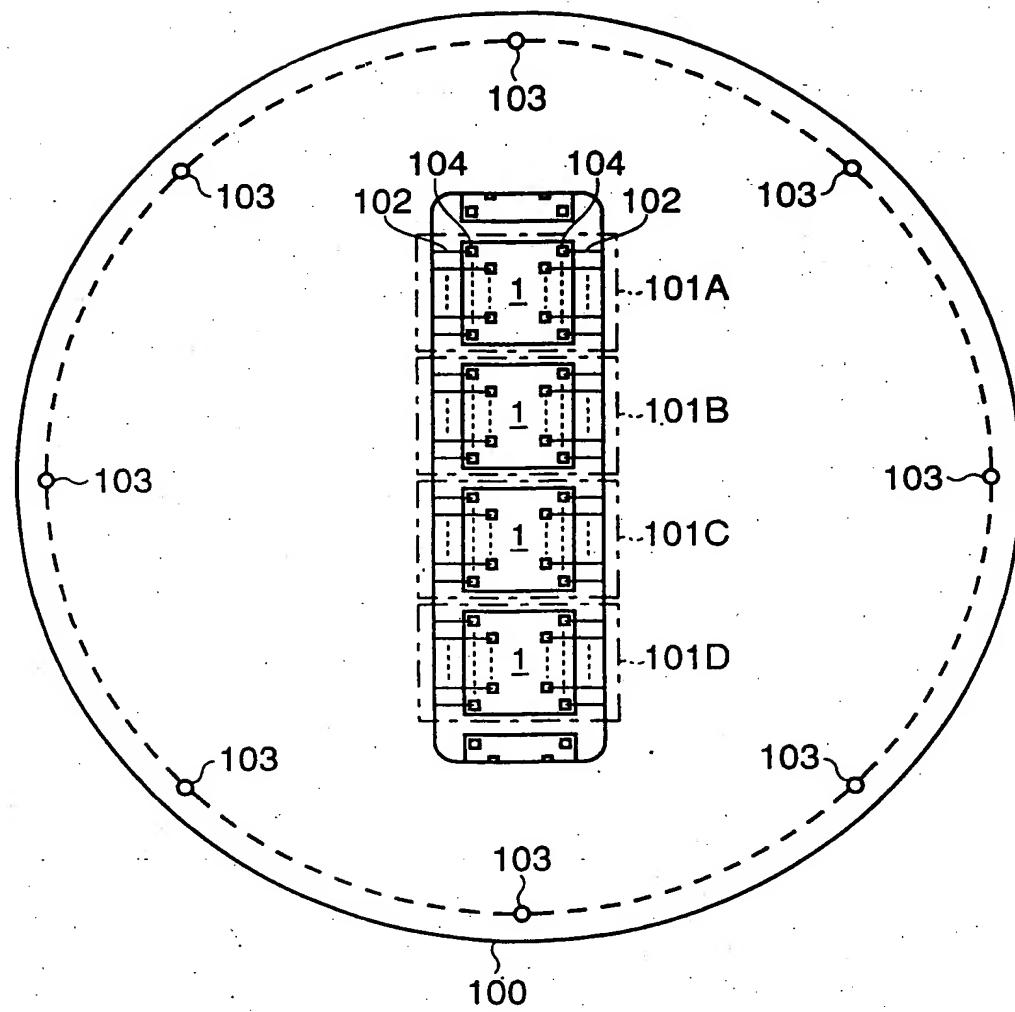


FIG. 4

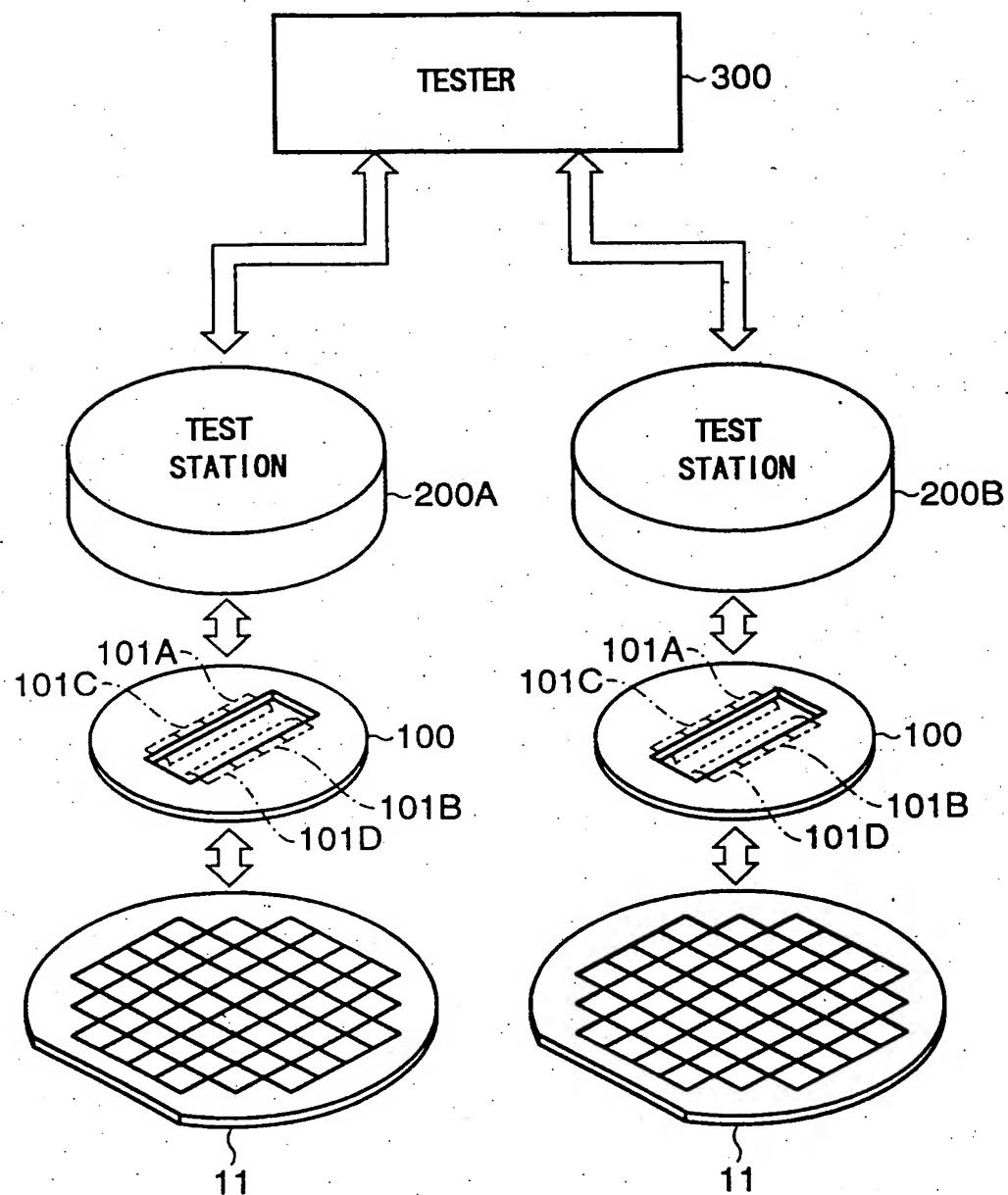


FIG. 5

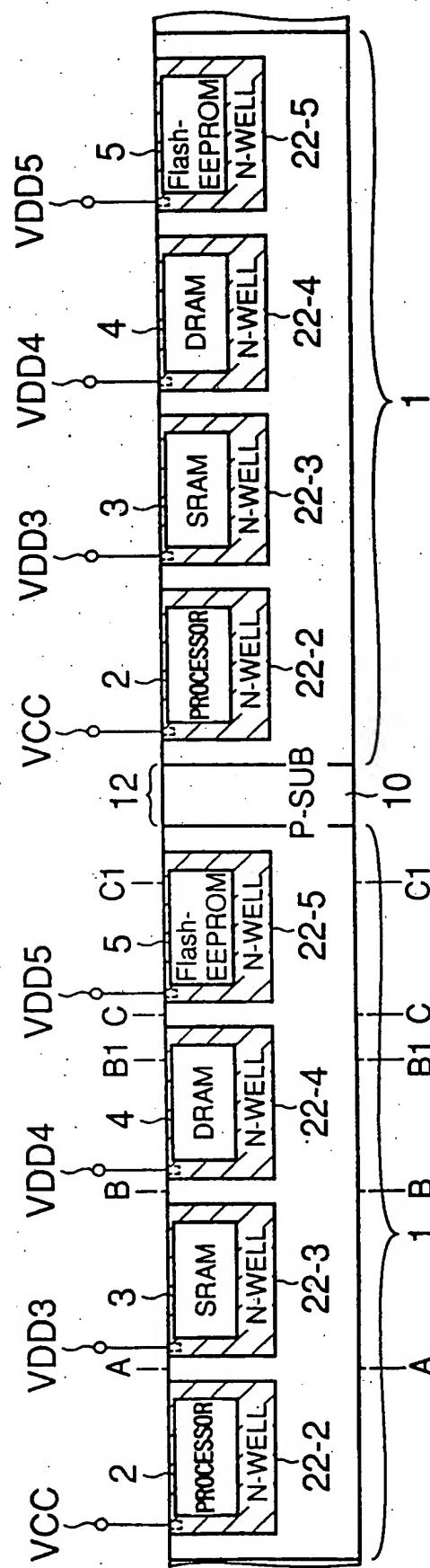


FIG. 6

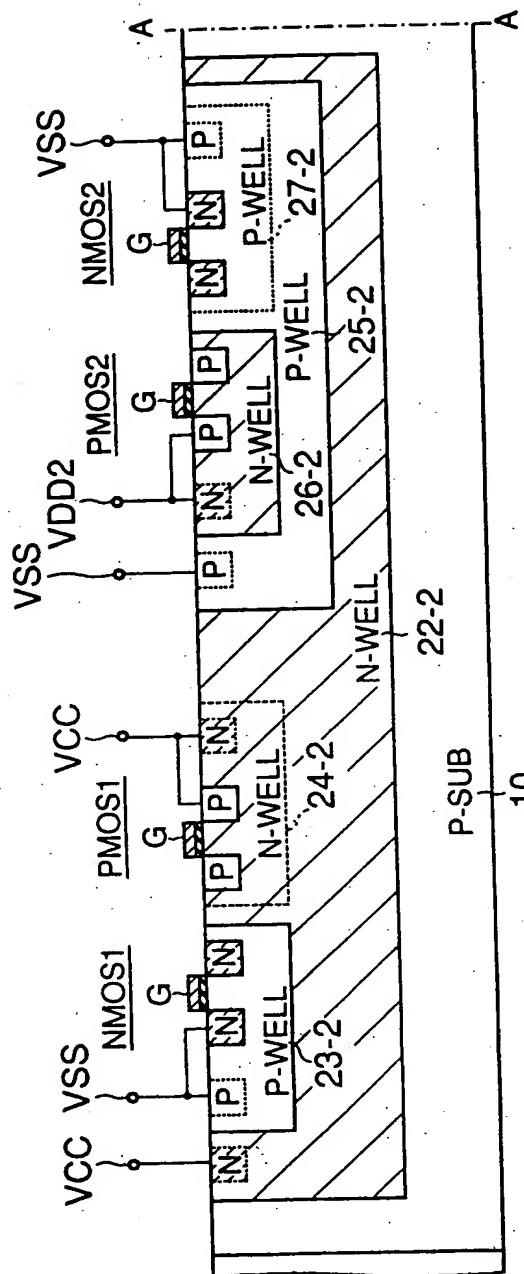


FIG. 7

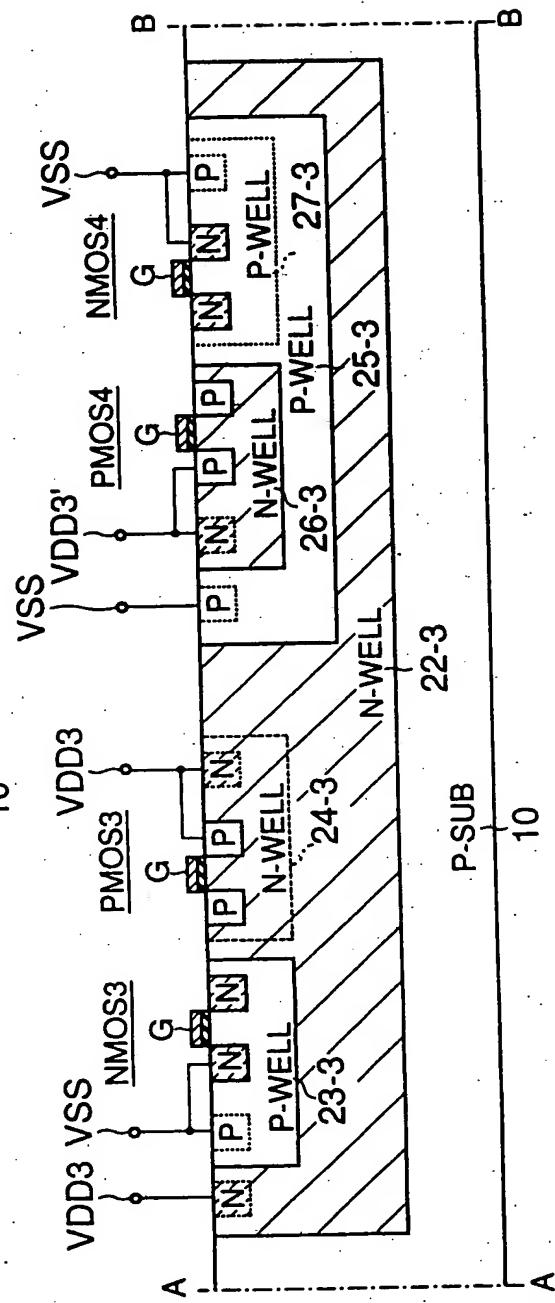
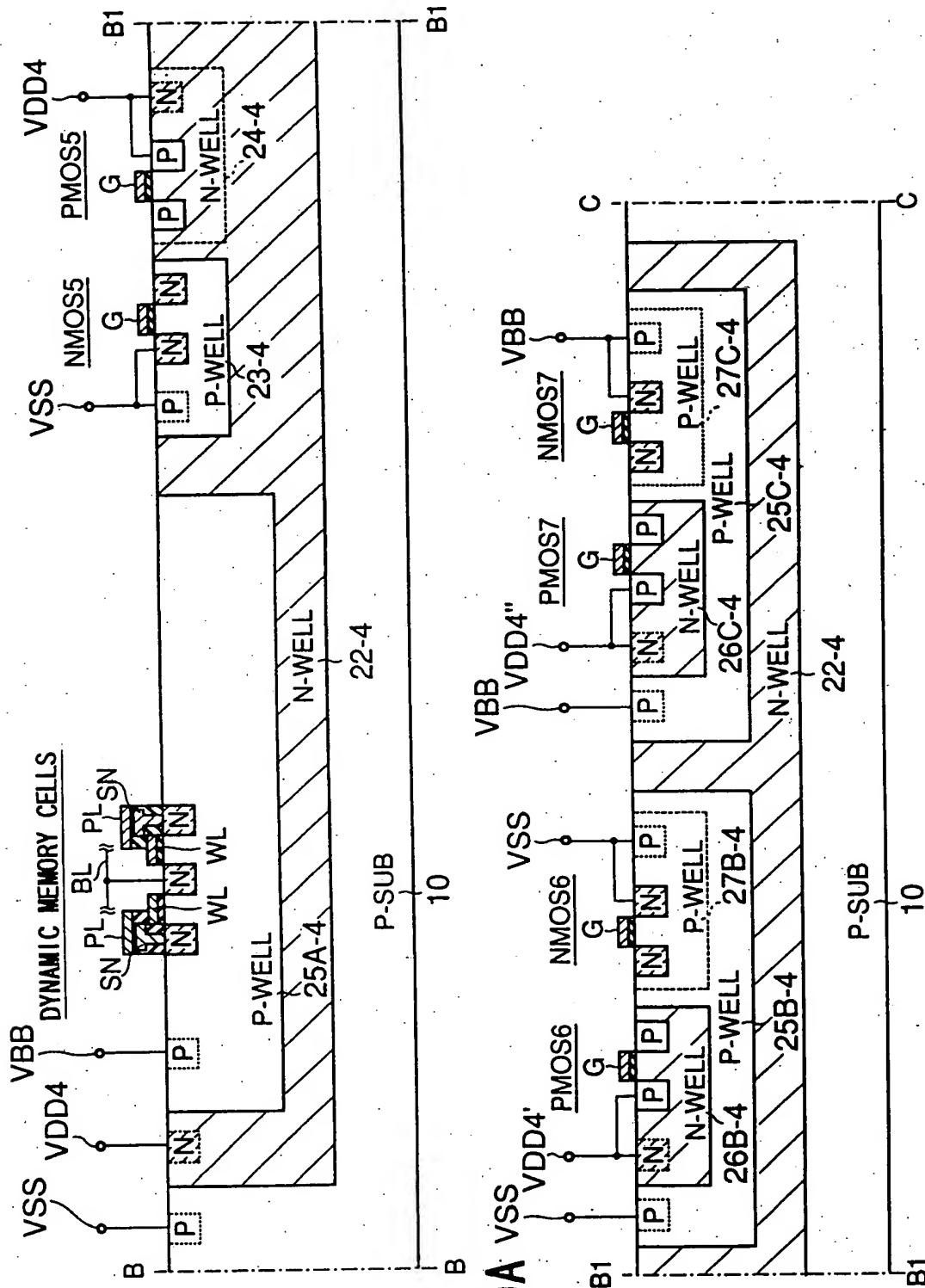


FIG. 8



E/G

FIG. 9B

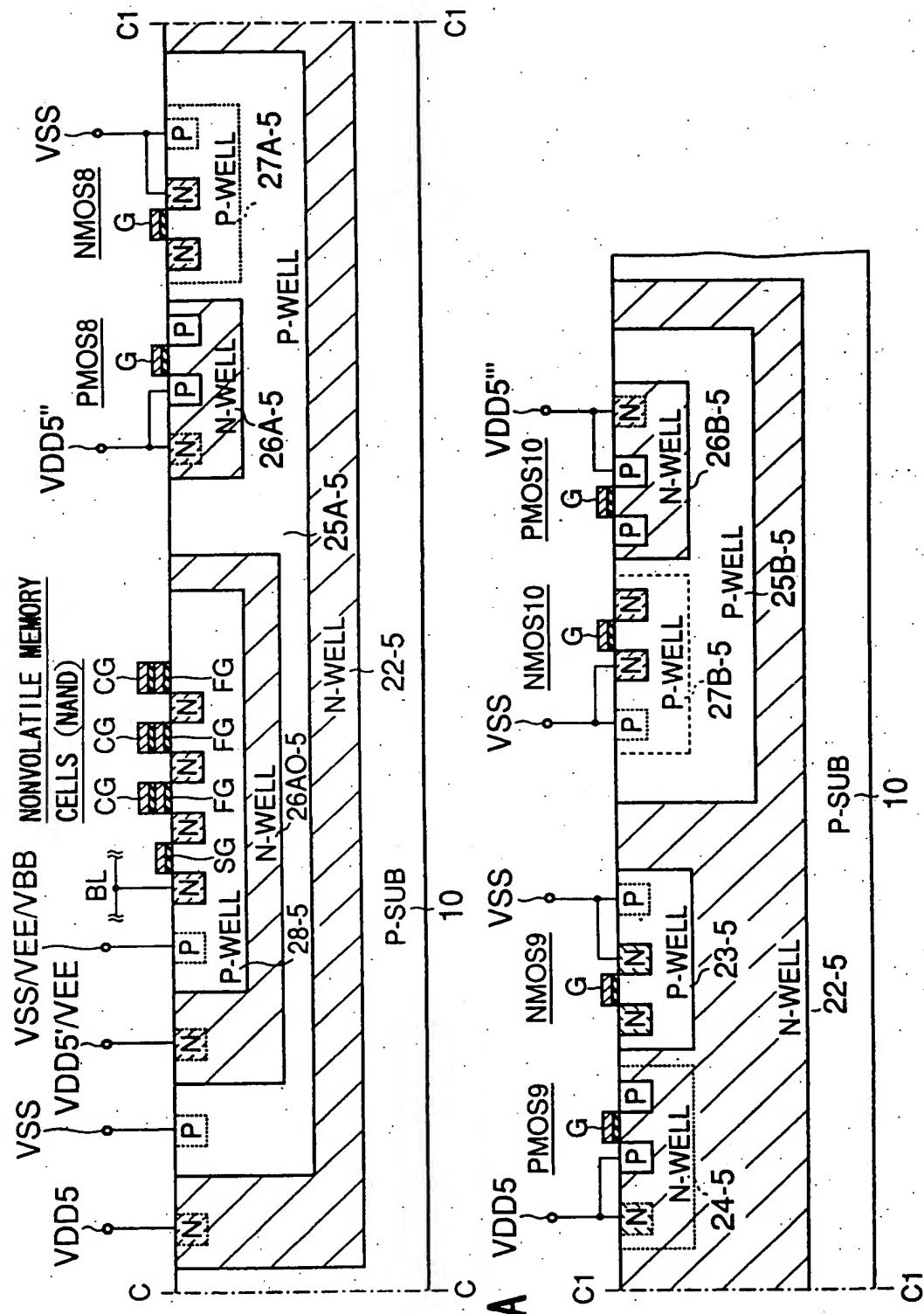


FIG. 10B

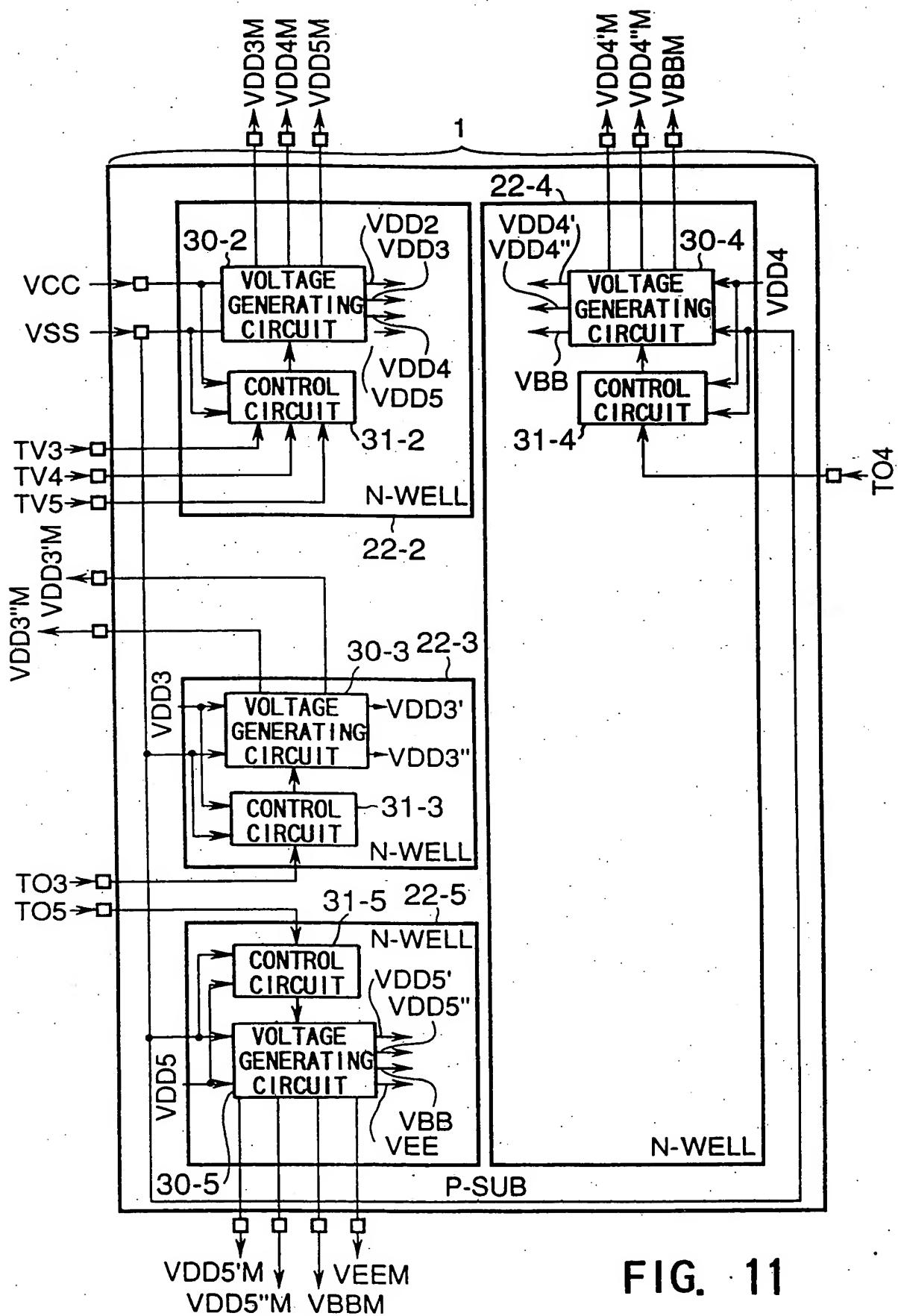
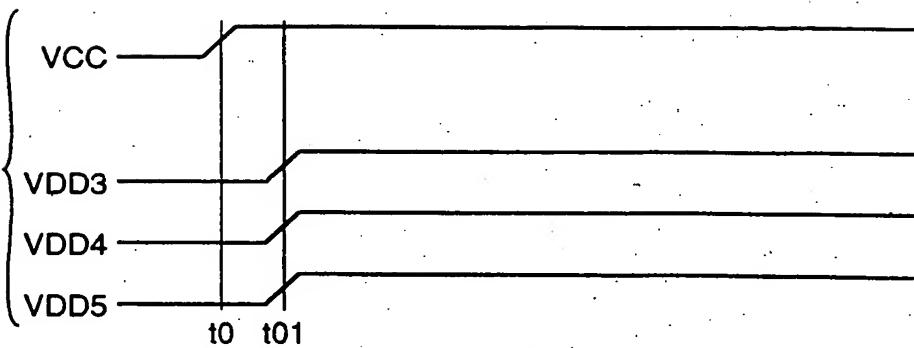
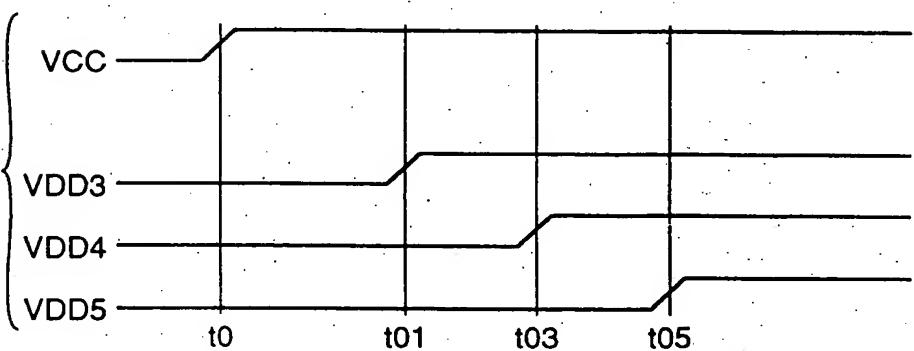


FIG. 11

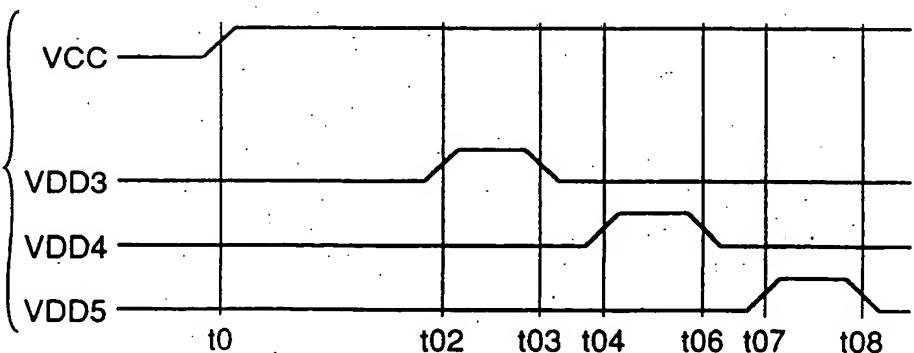
**FIG. 12A**



**FIG. 12B**



**FIG. 12C**



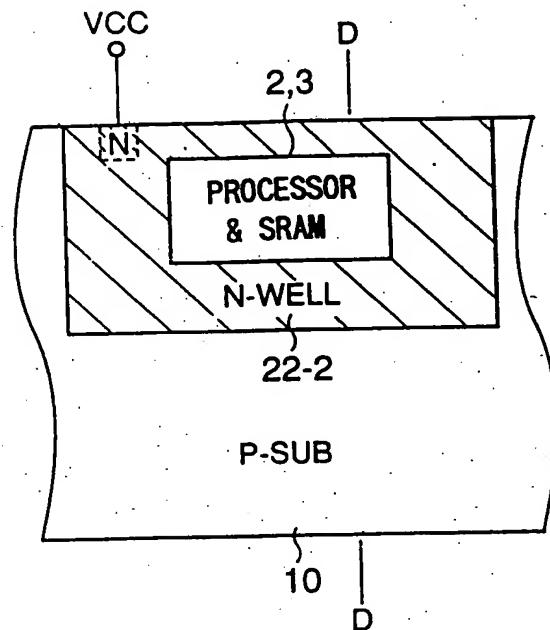


FIG. 13

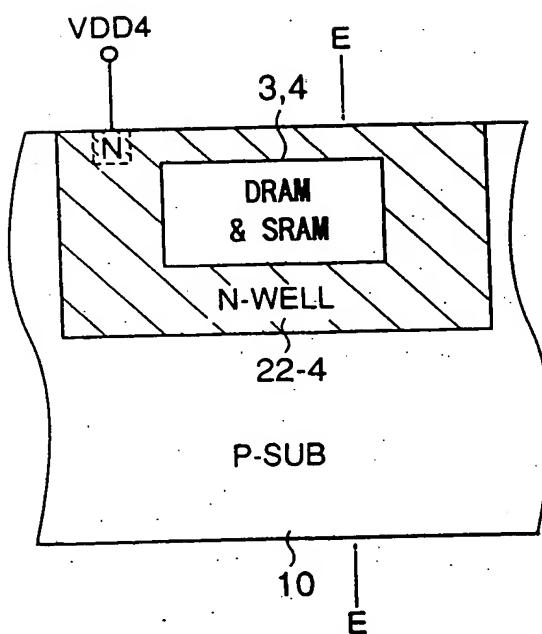
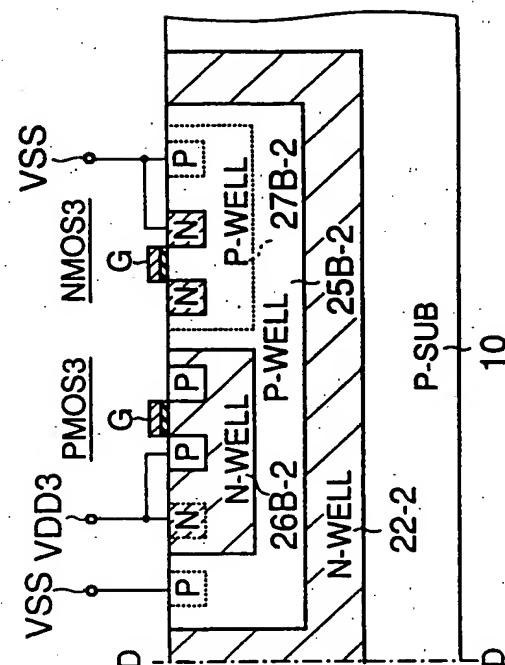
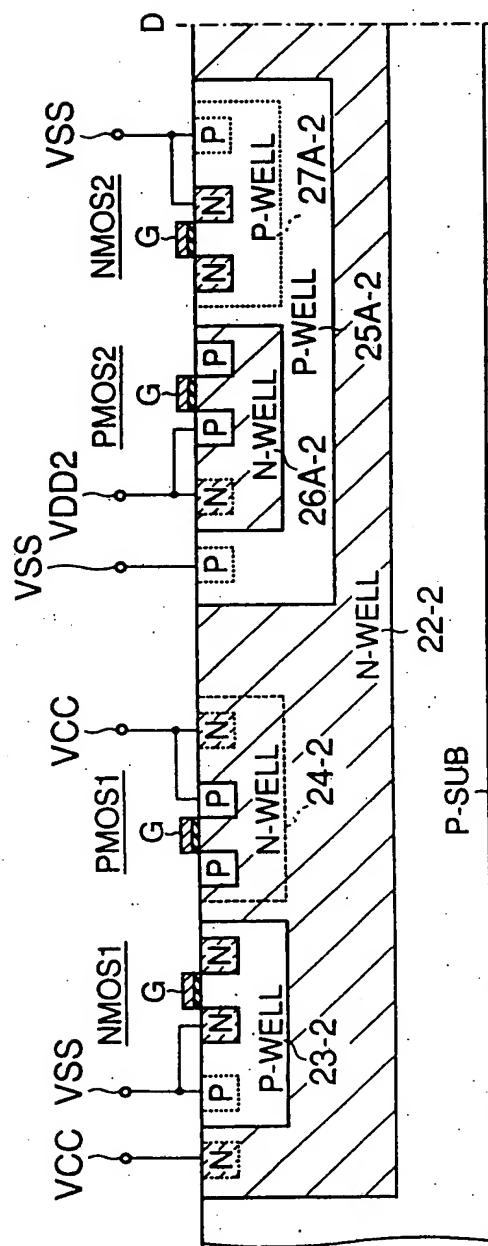


FIG. 15



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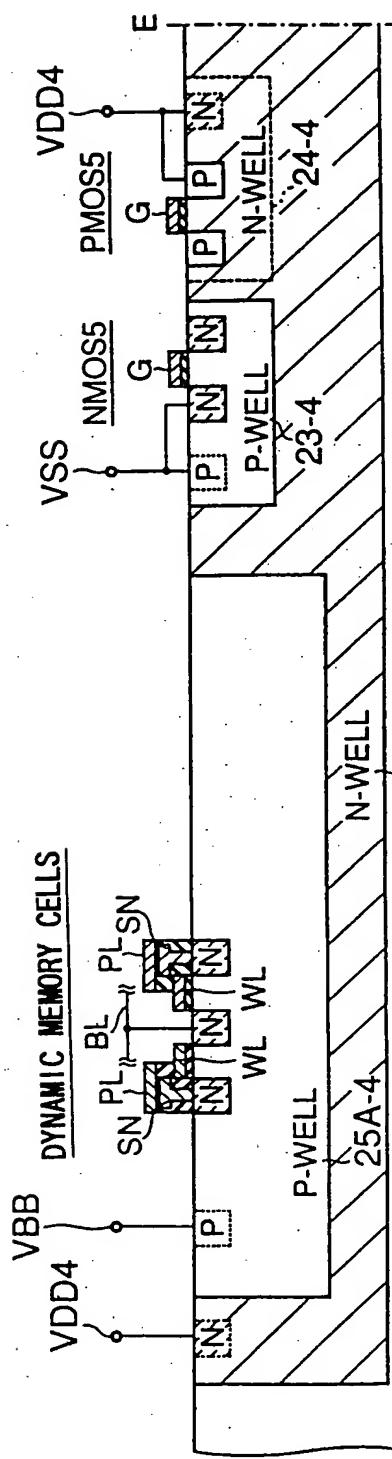


FIG. 16A

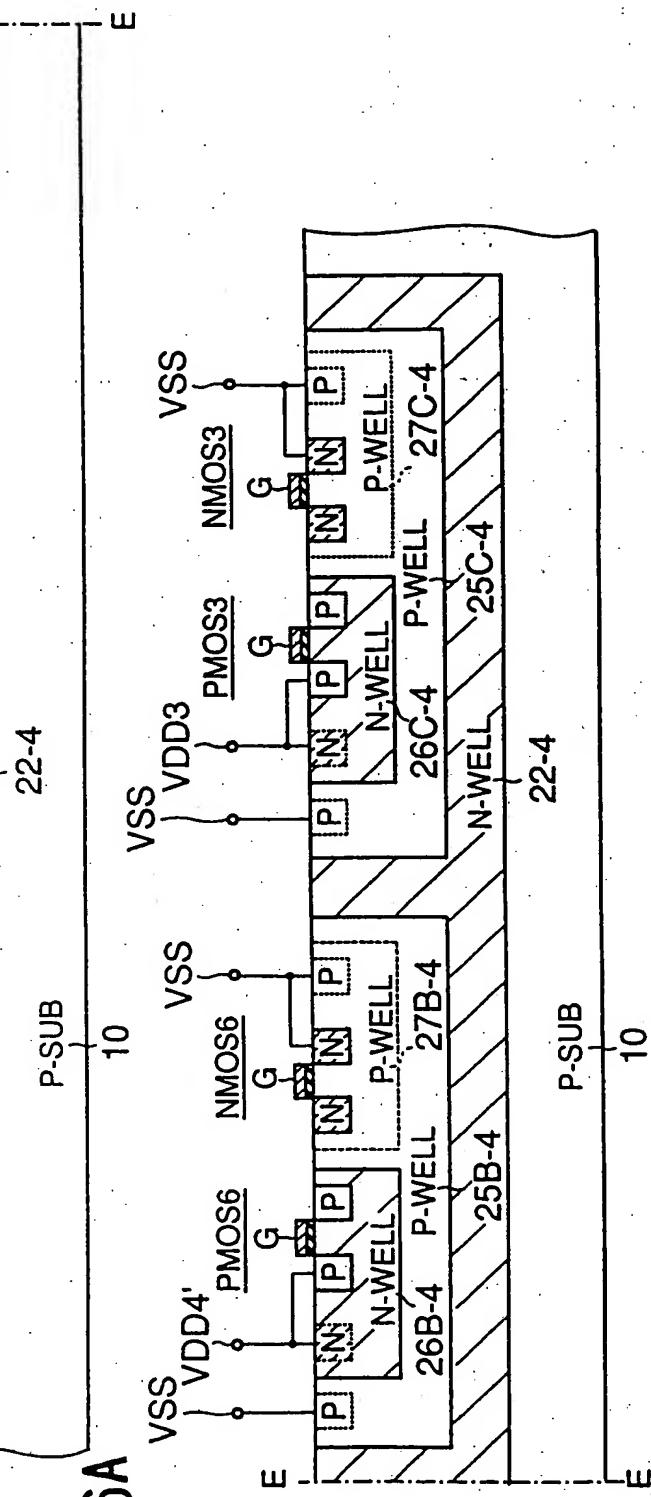


FIG. 16B

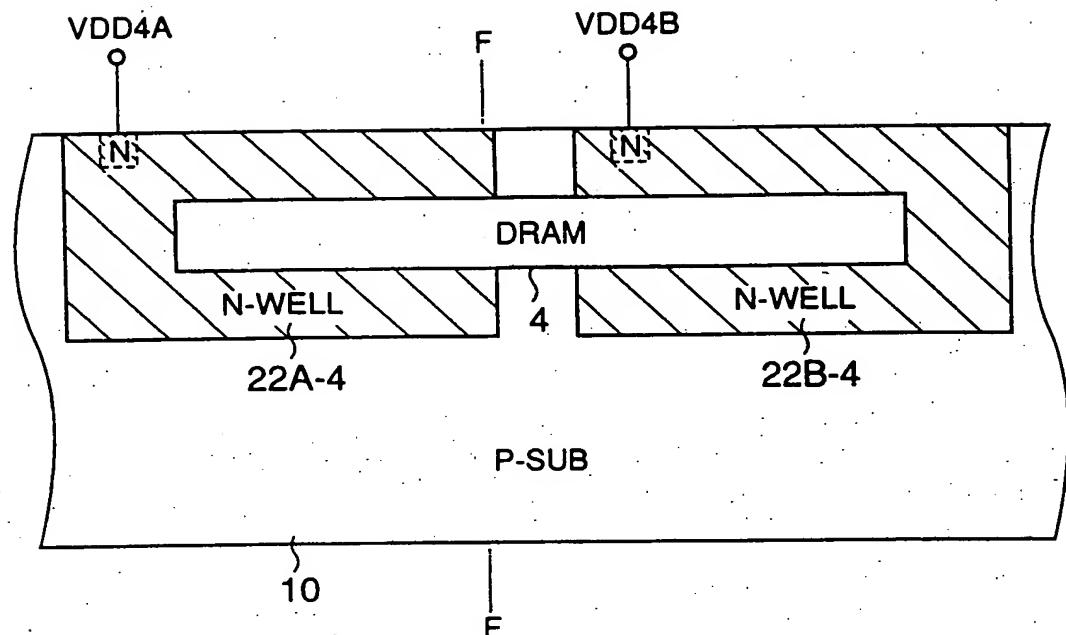


FIG. 17

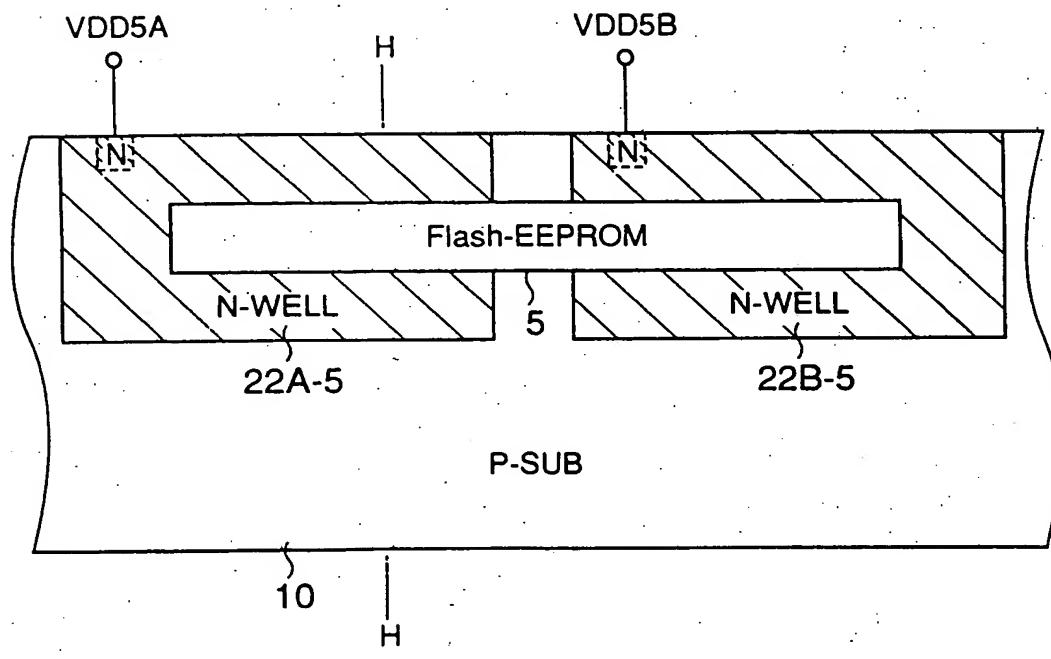
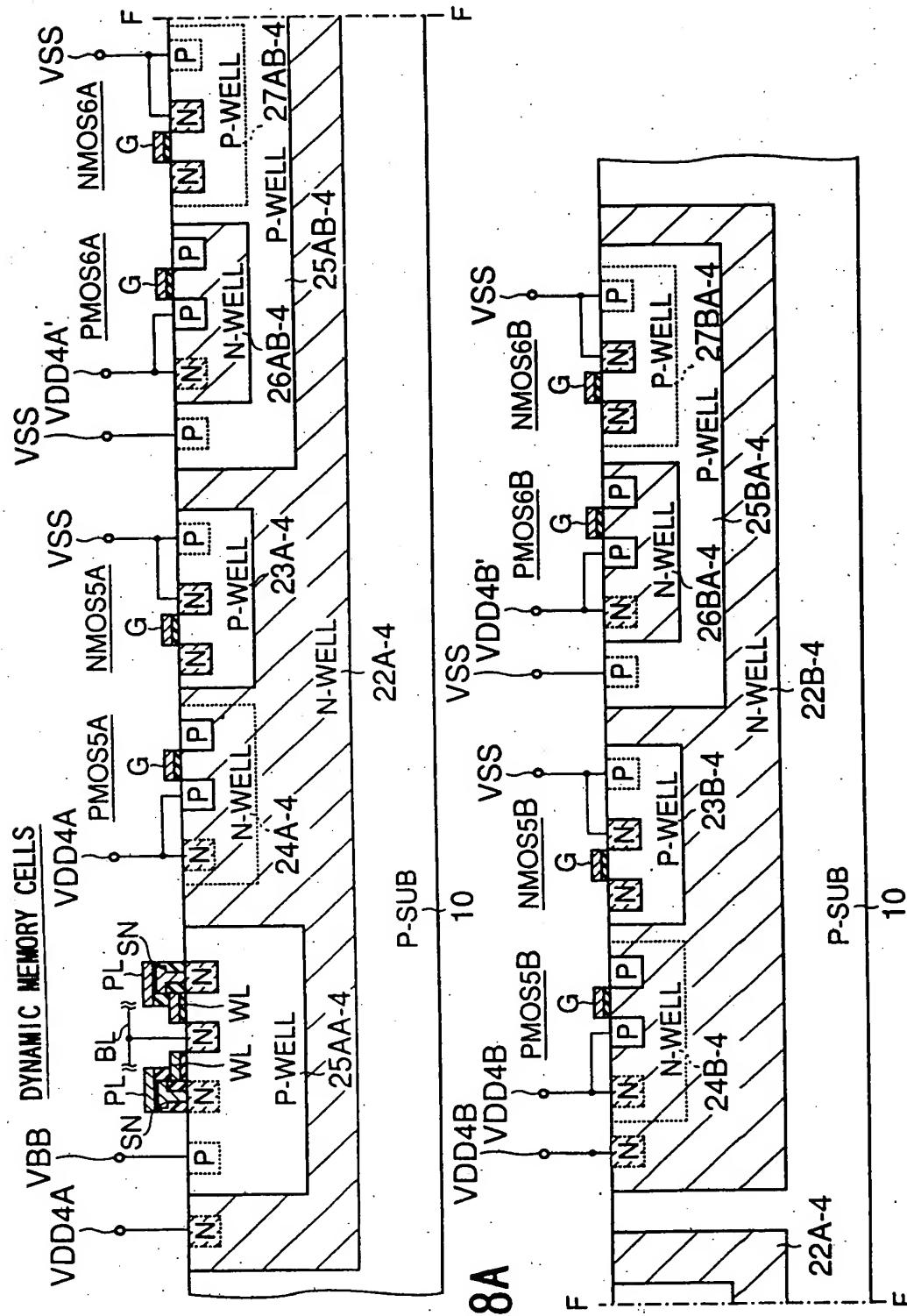
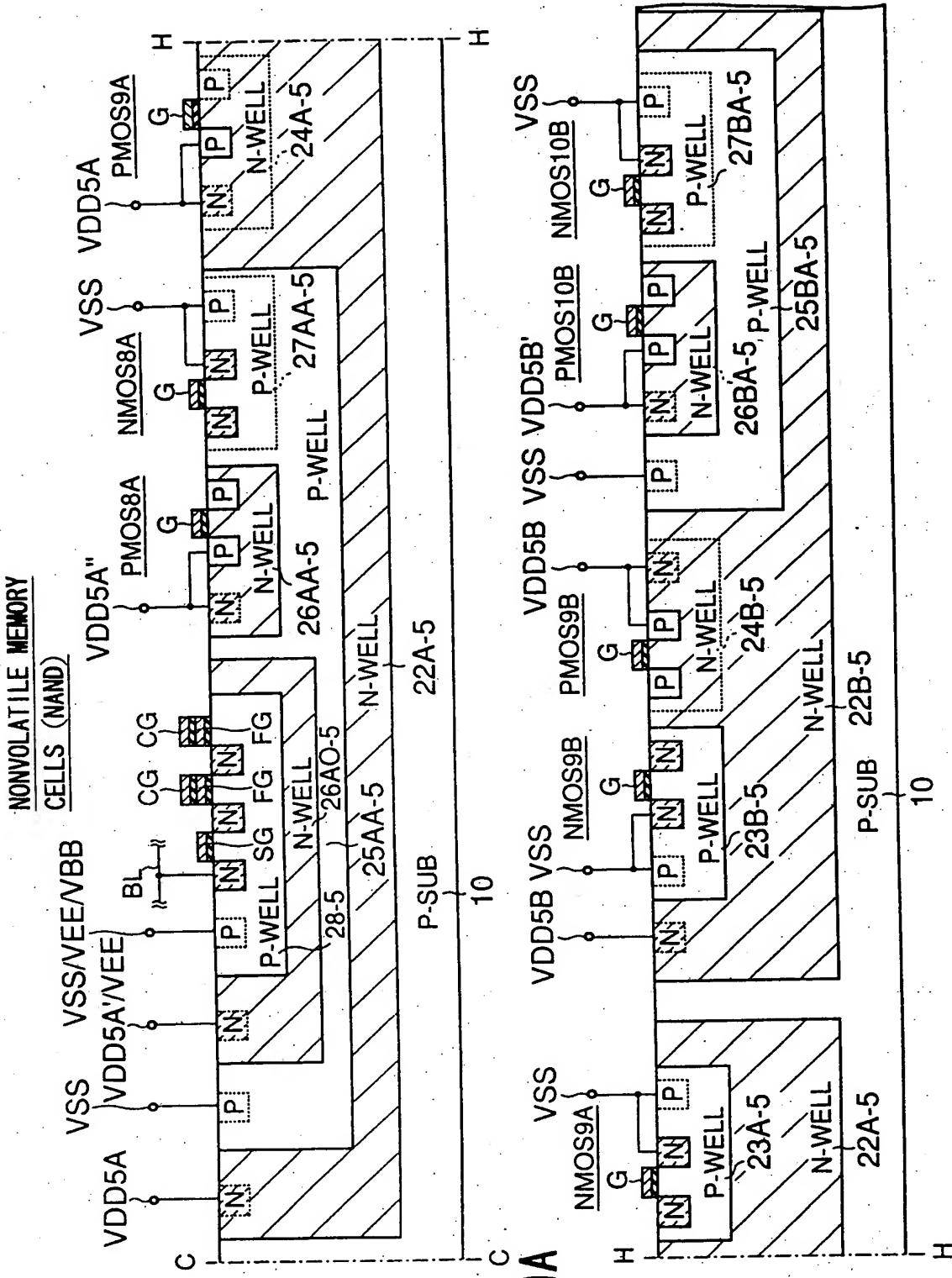


FIG. 19



**FIG. 18B**

**FIG. 18B**



५

FIG. 20B

FIG. 21A

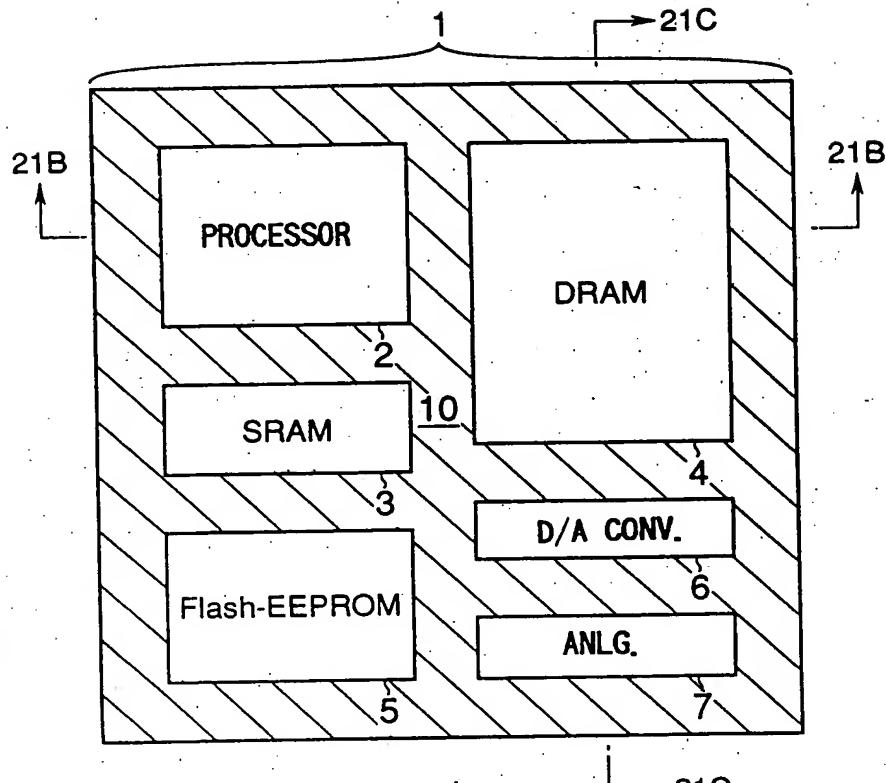


FIG. 21B

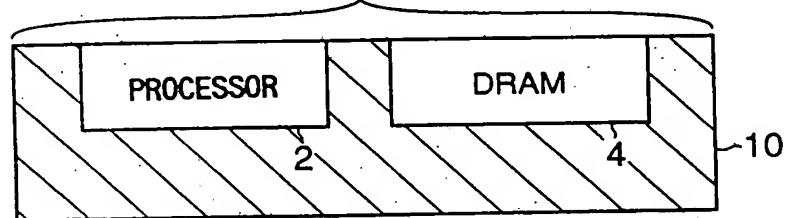
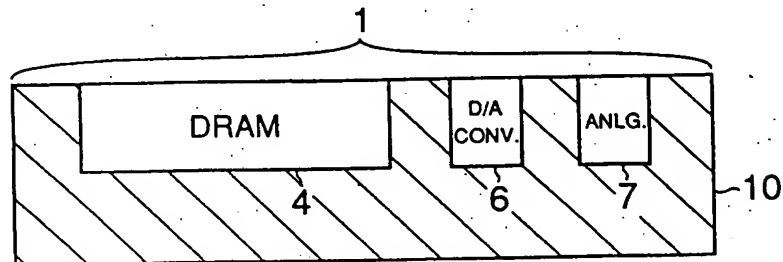


FIG. 21C



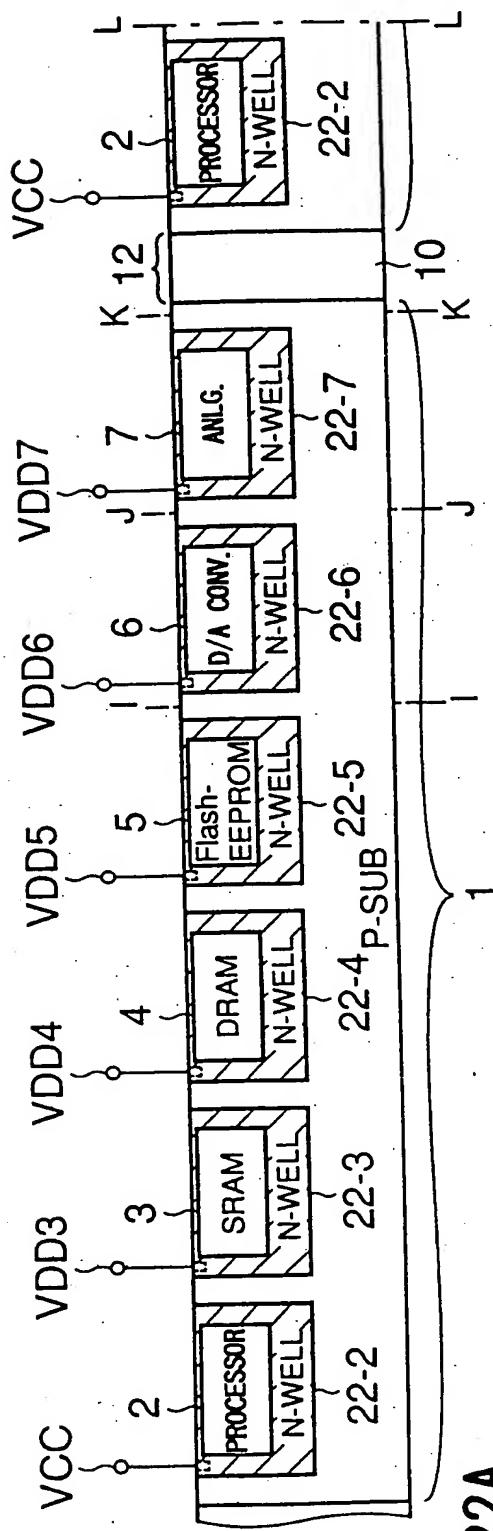


FIG. 22A

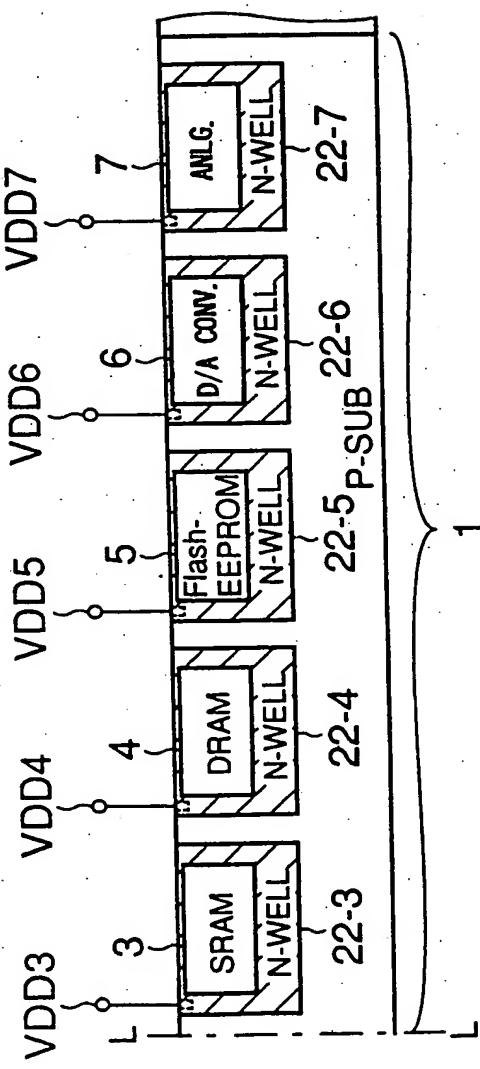


FIG. 22B

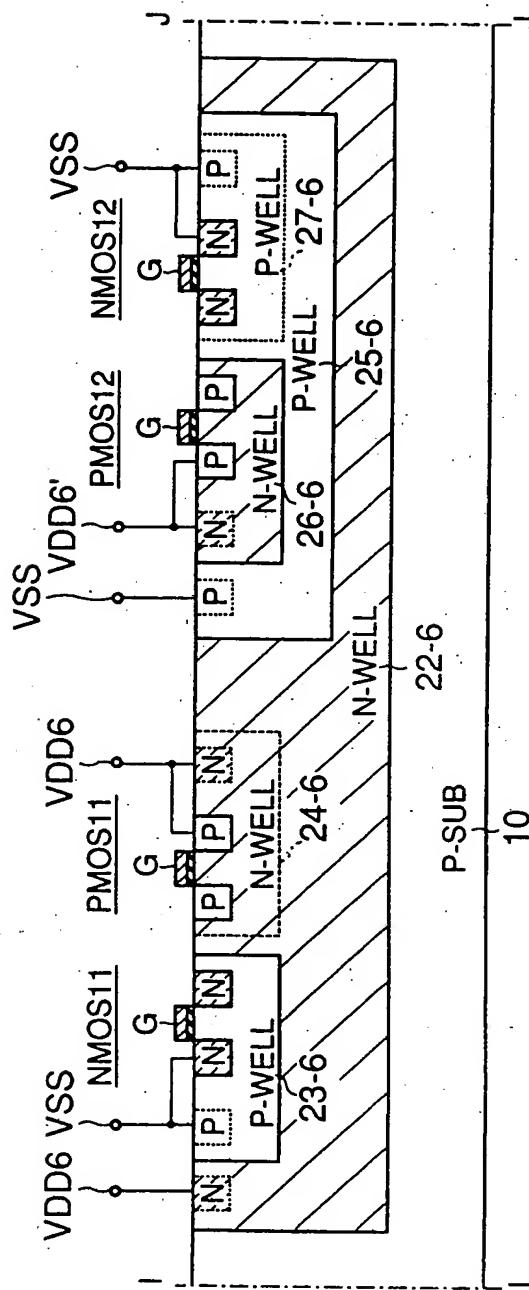


FIG. 23

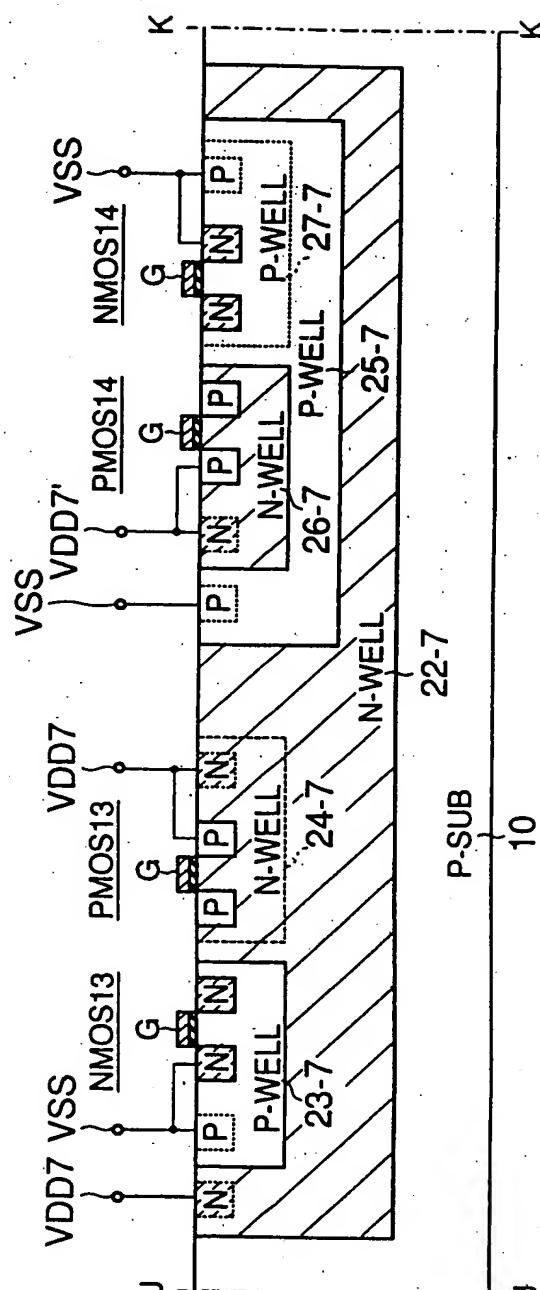


FIG. 24

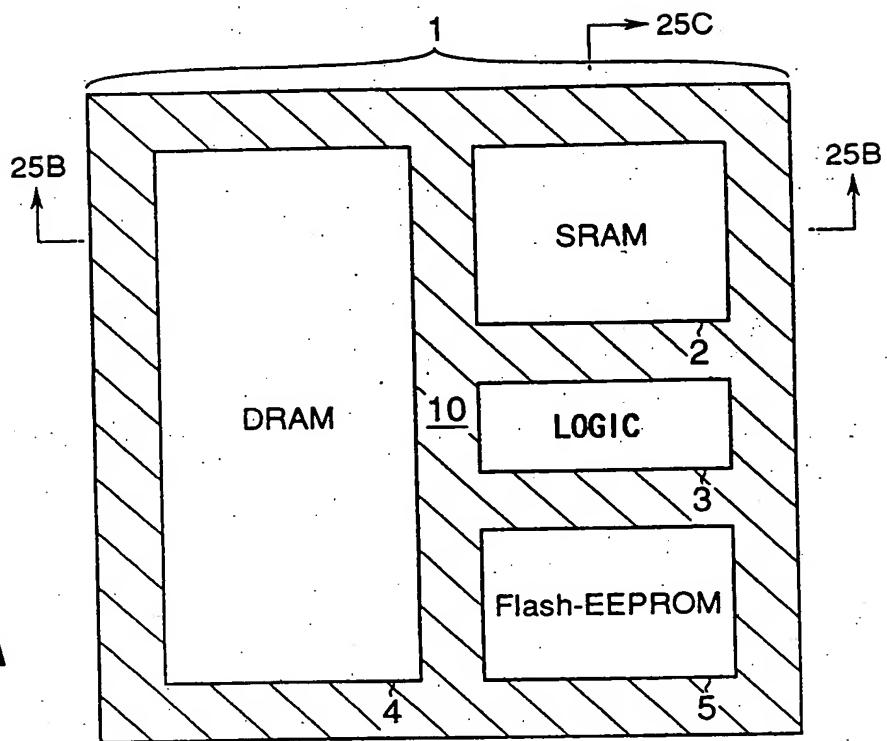


FIG. 25A

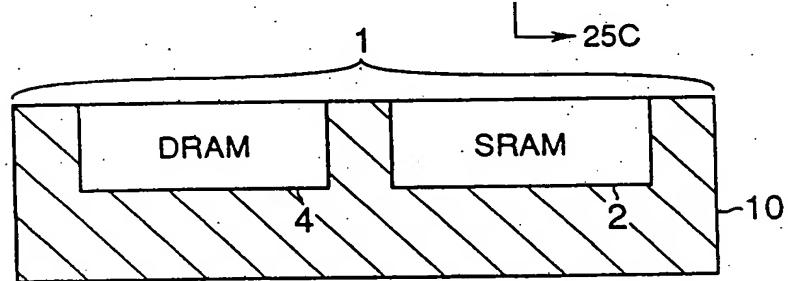


FIG. 25B

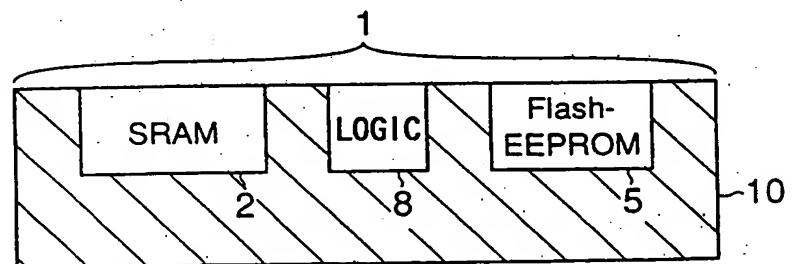


FIG. 25C

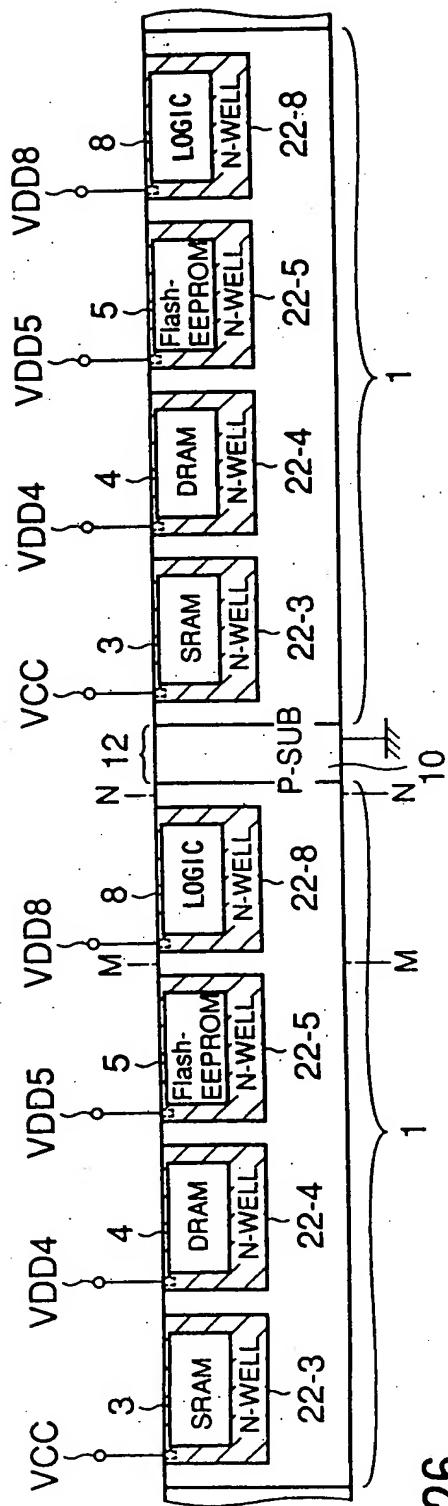
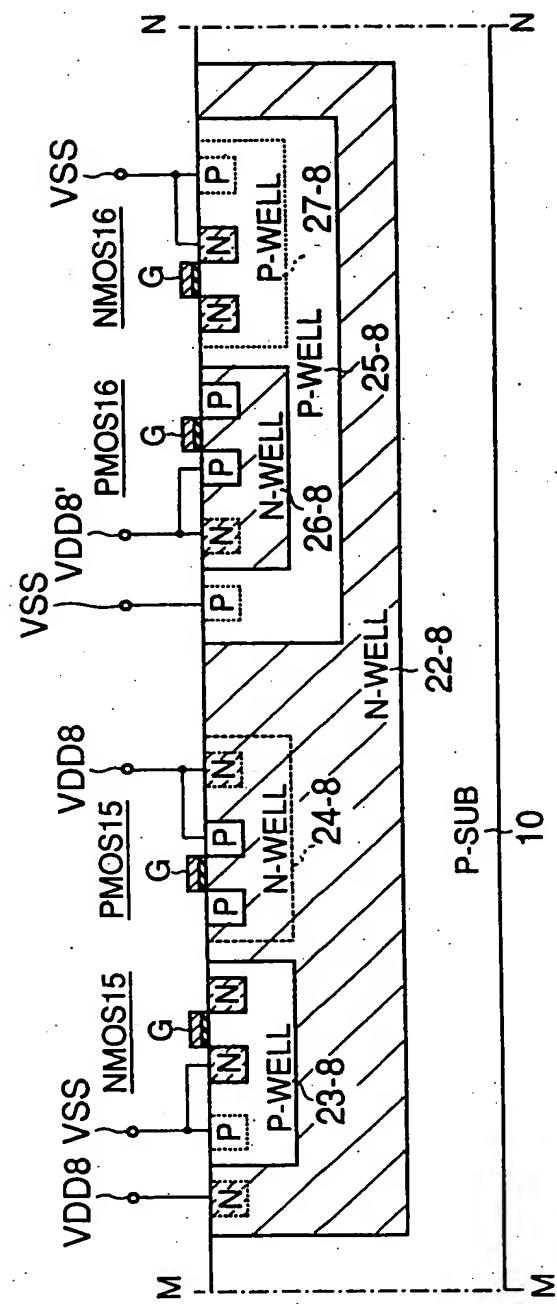


FIG. 26



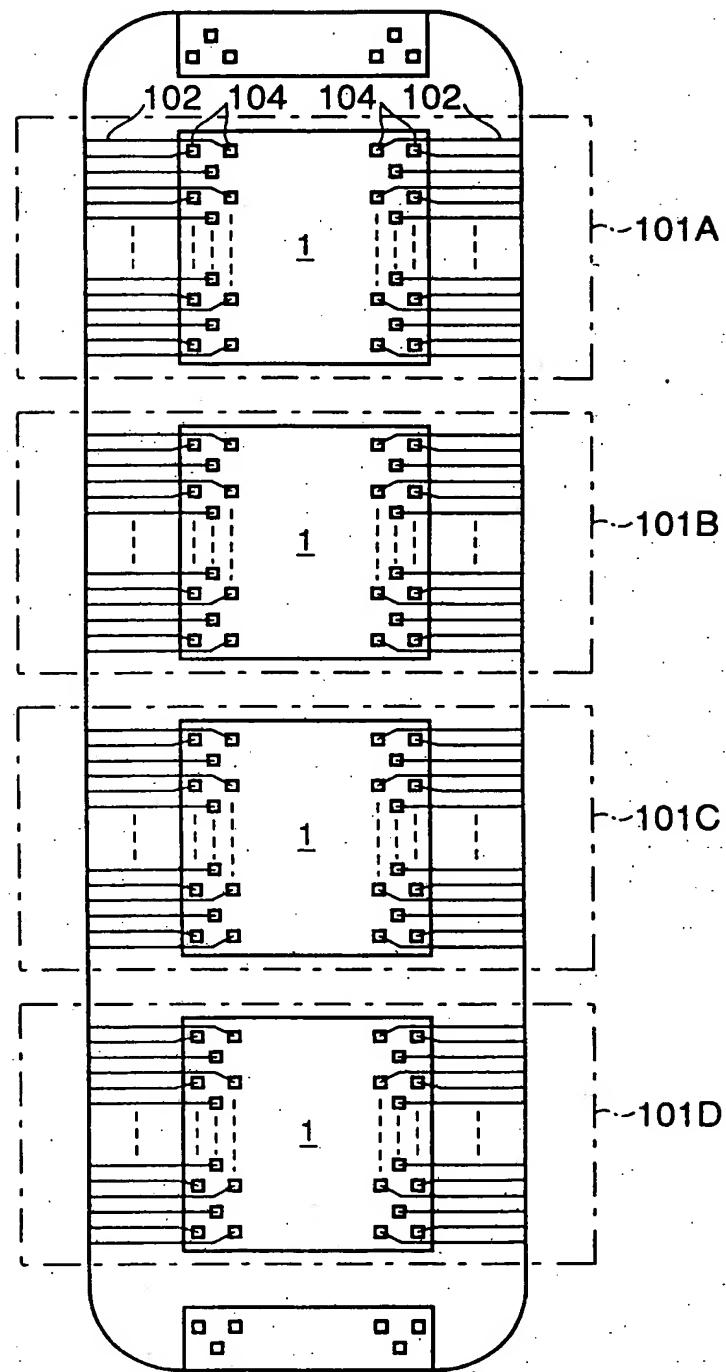


FIG. 28

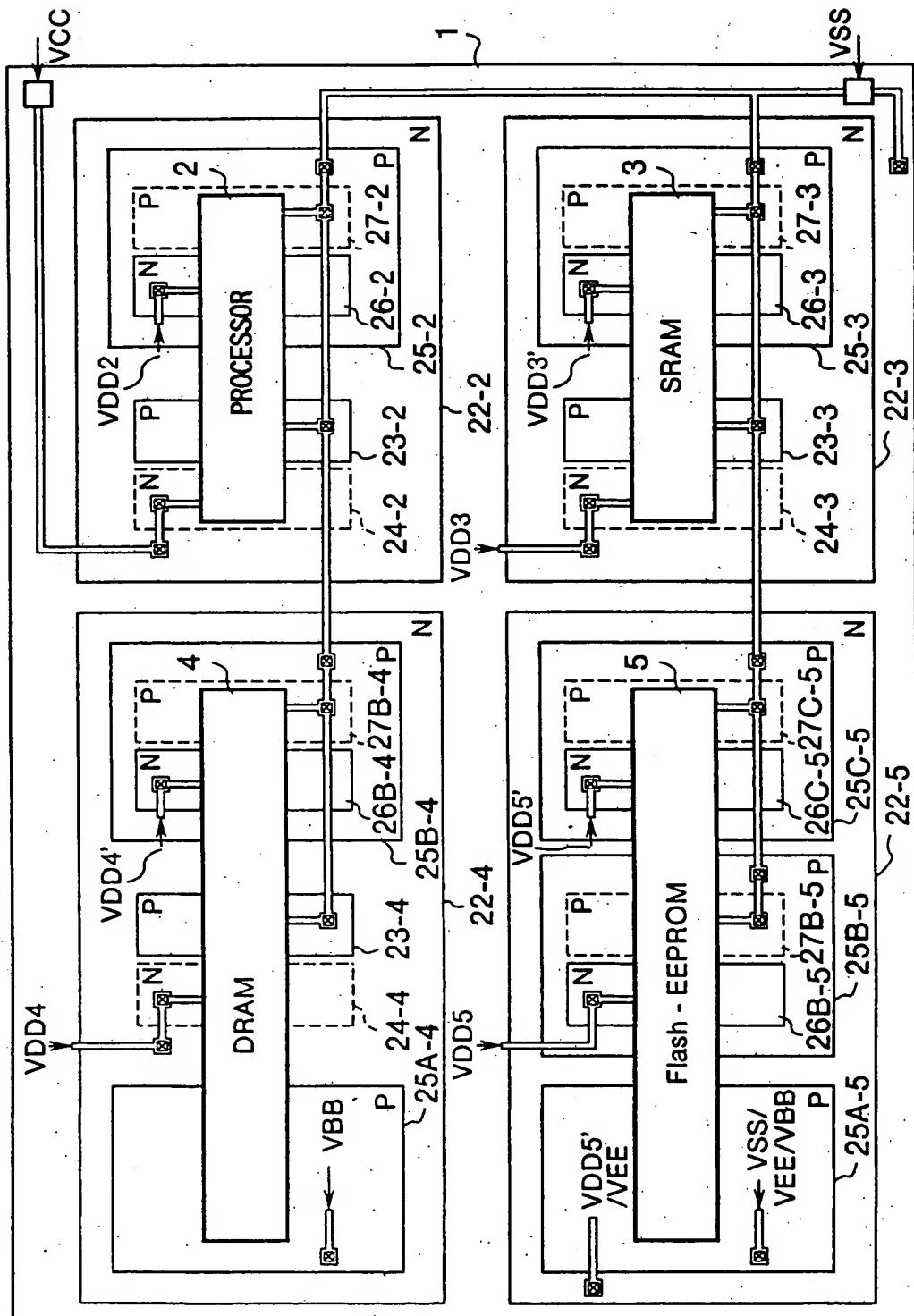


FIG. 29

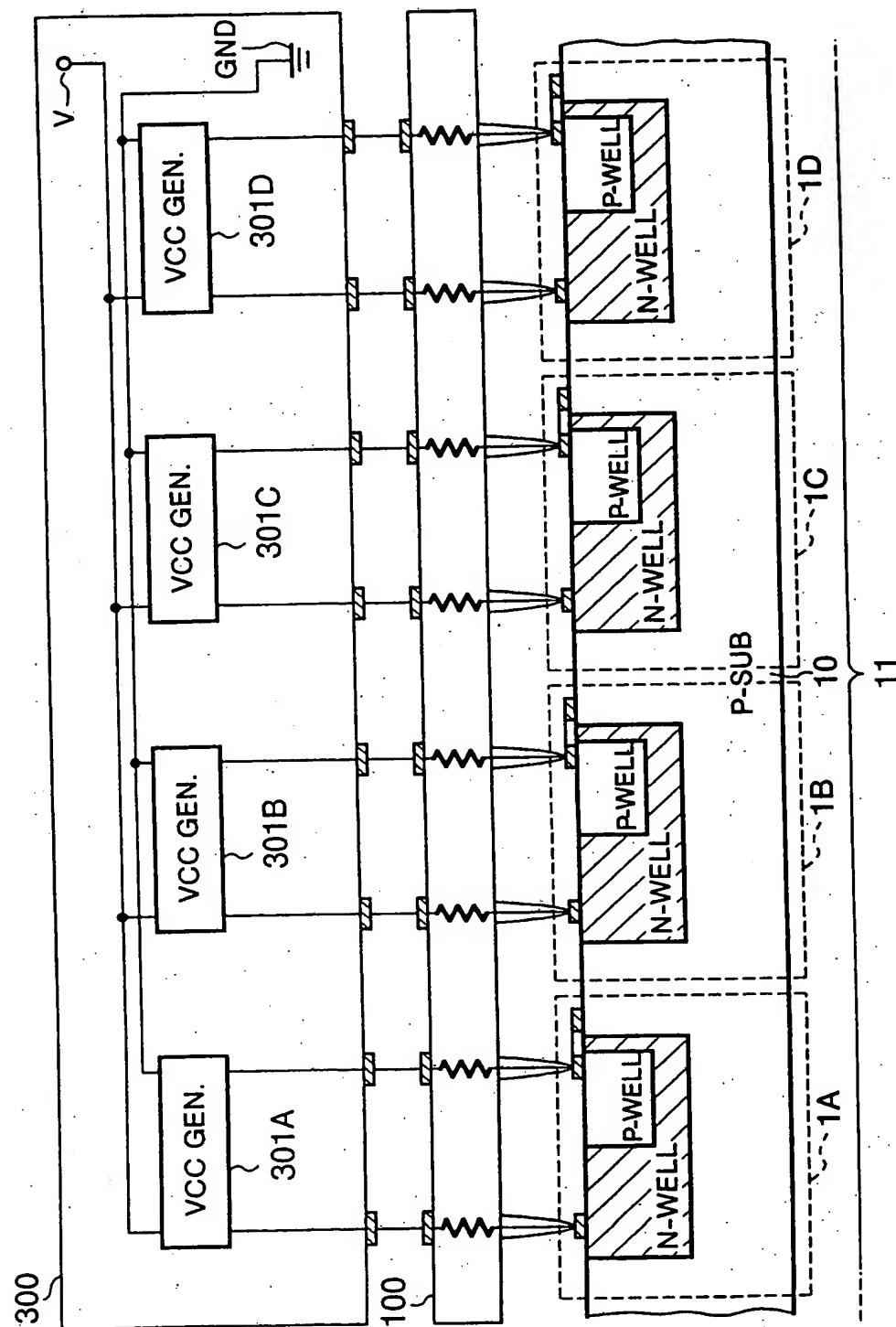


FIG. 30

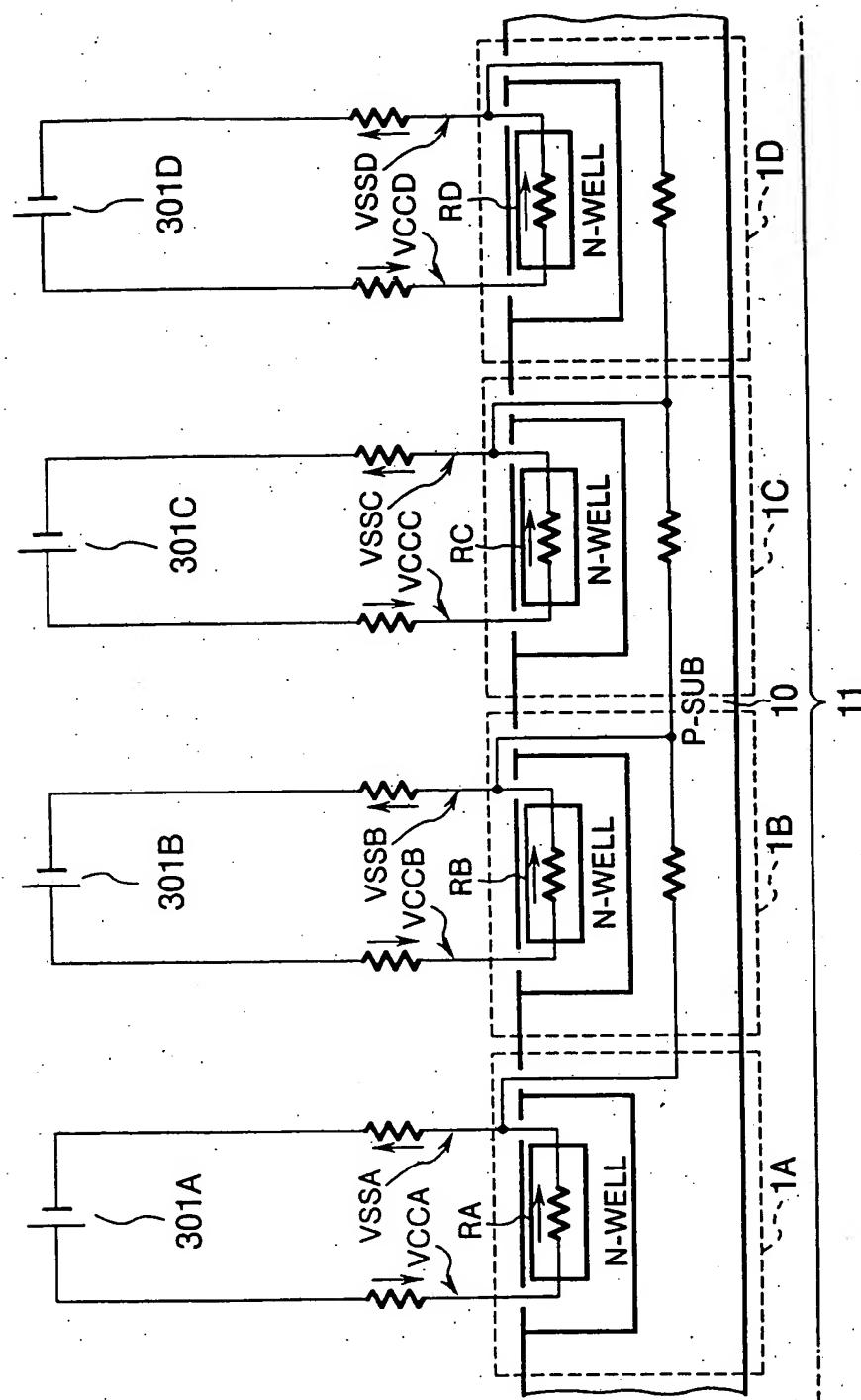


FIG. 31

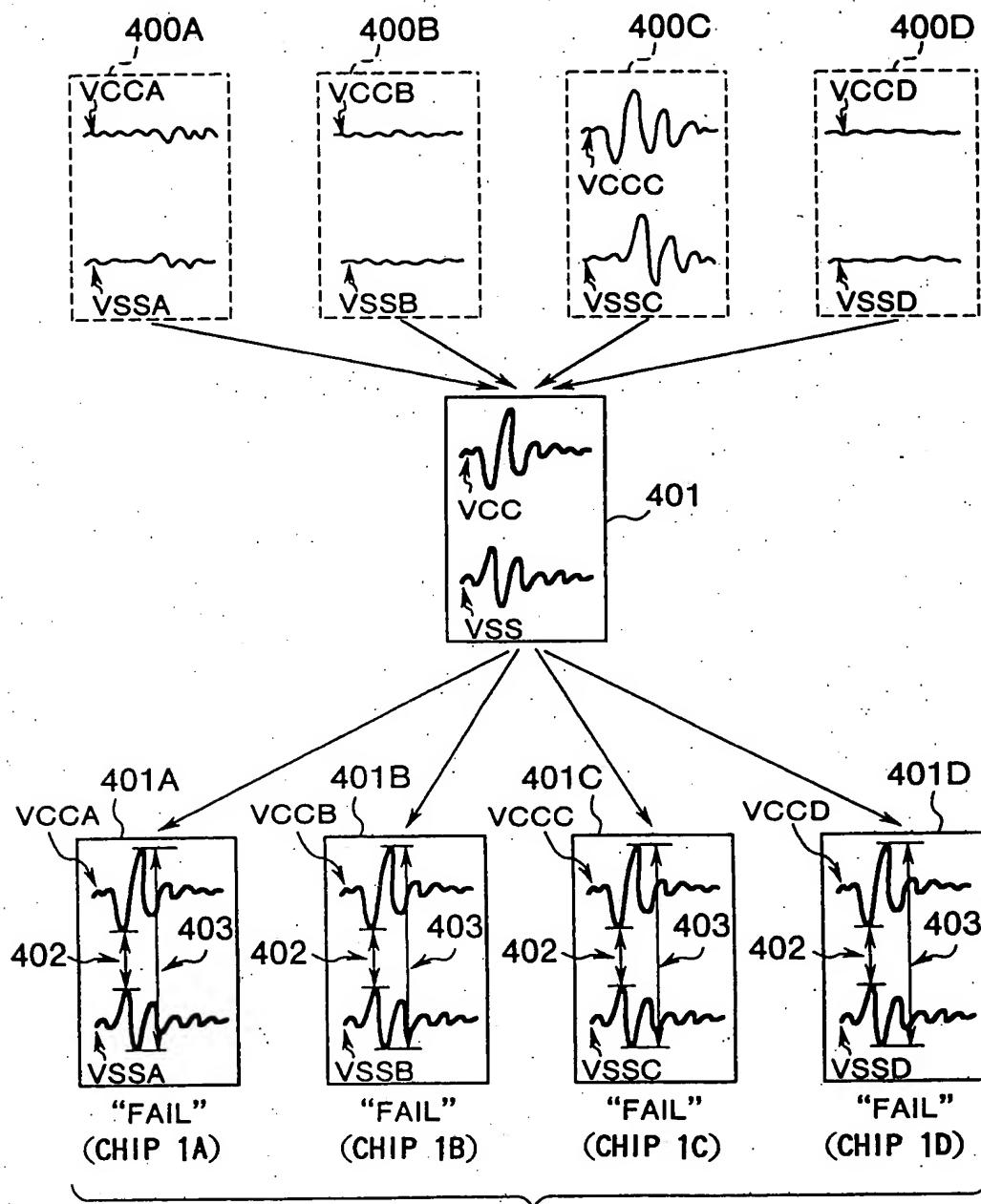
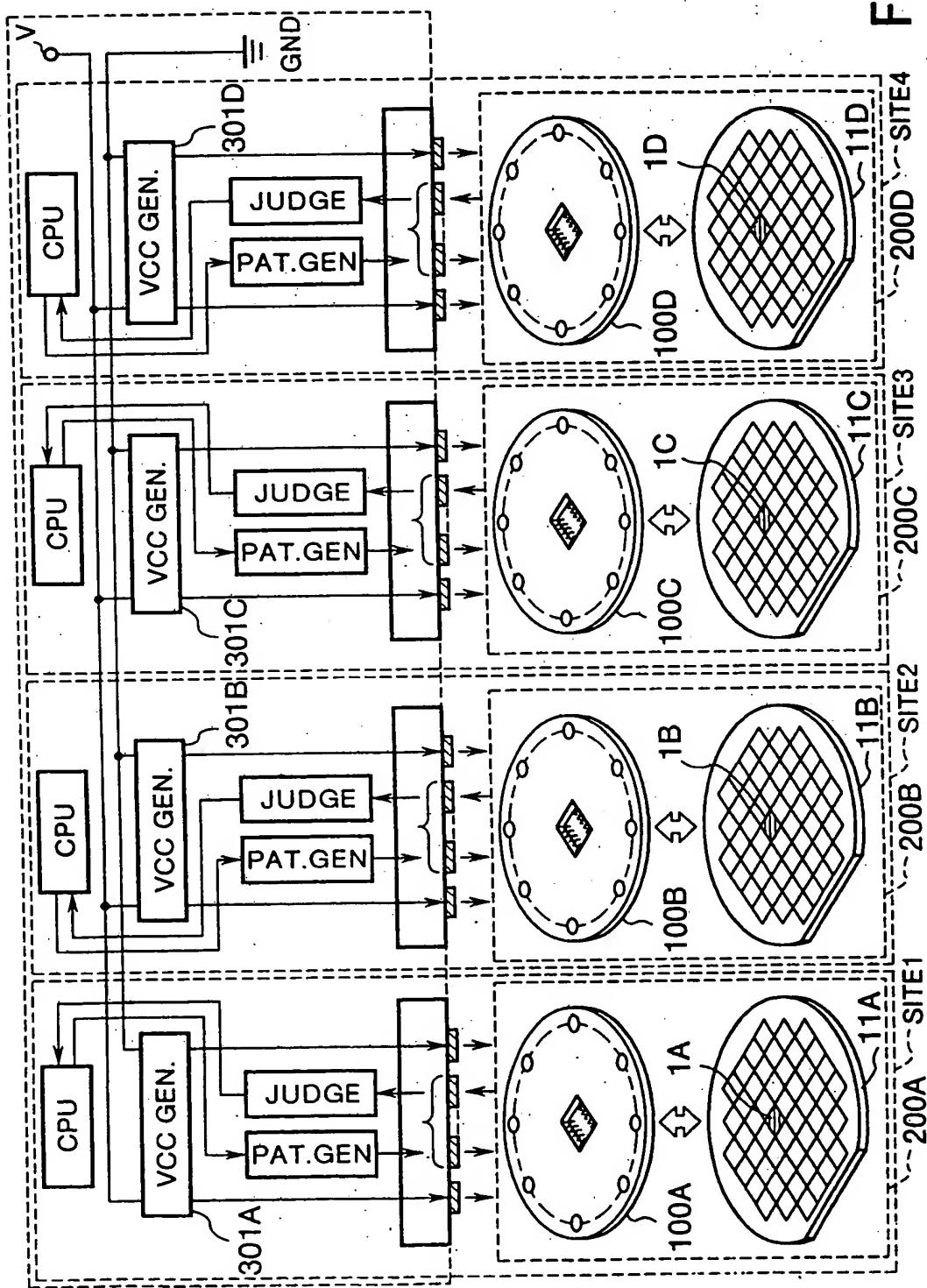


FIG. 32

FIG. 33



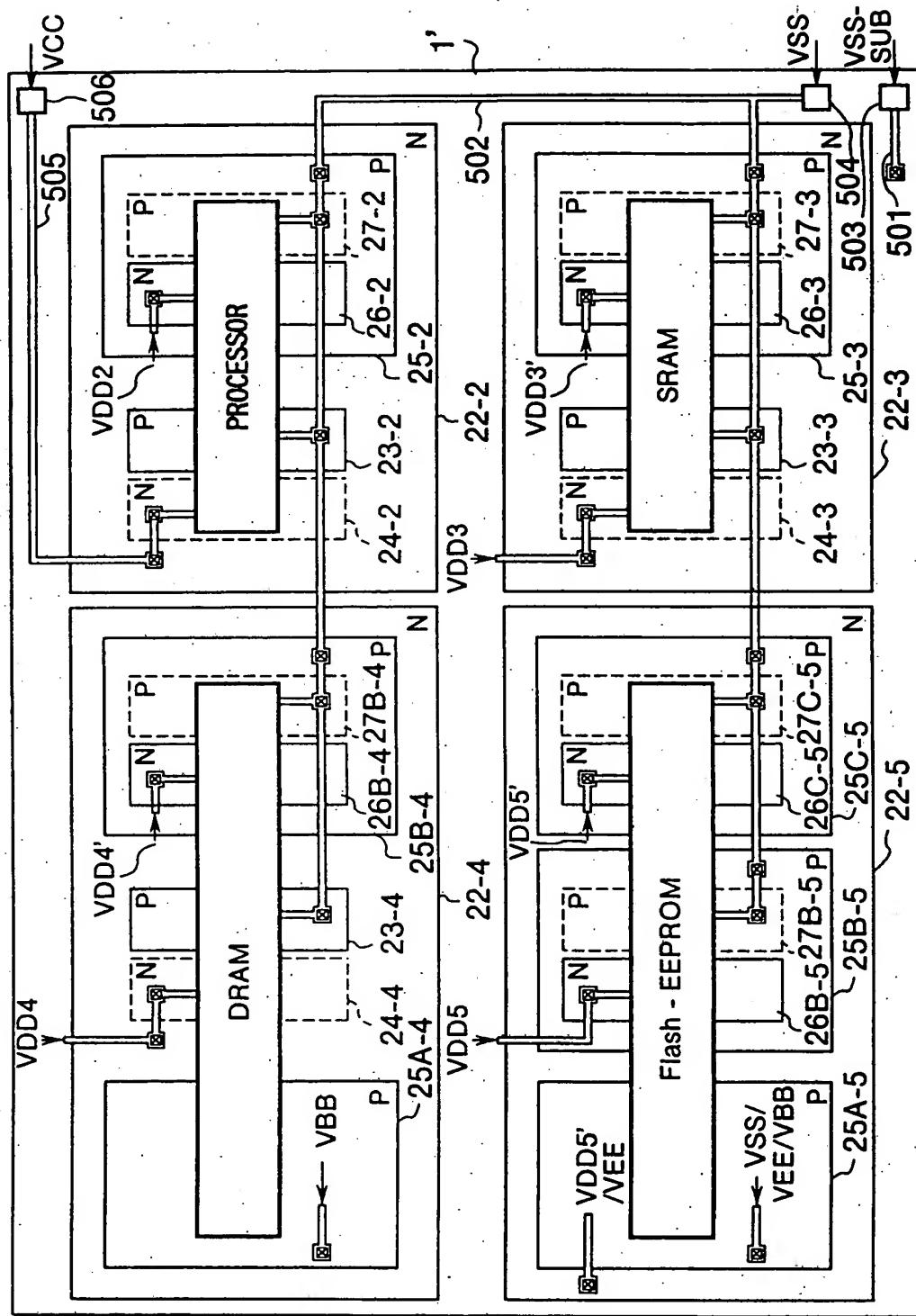


FIG. 34

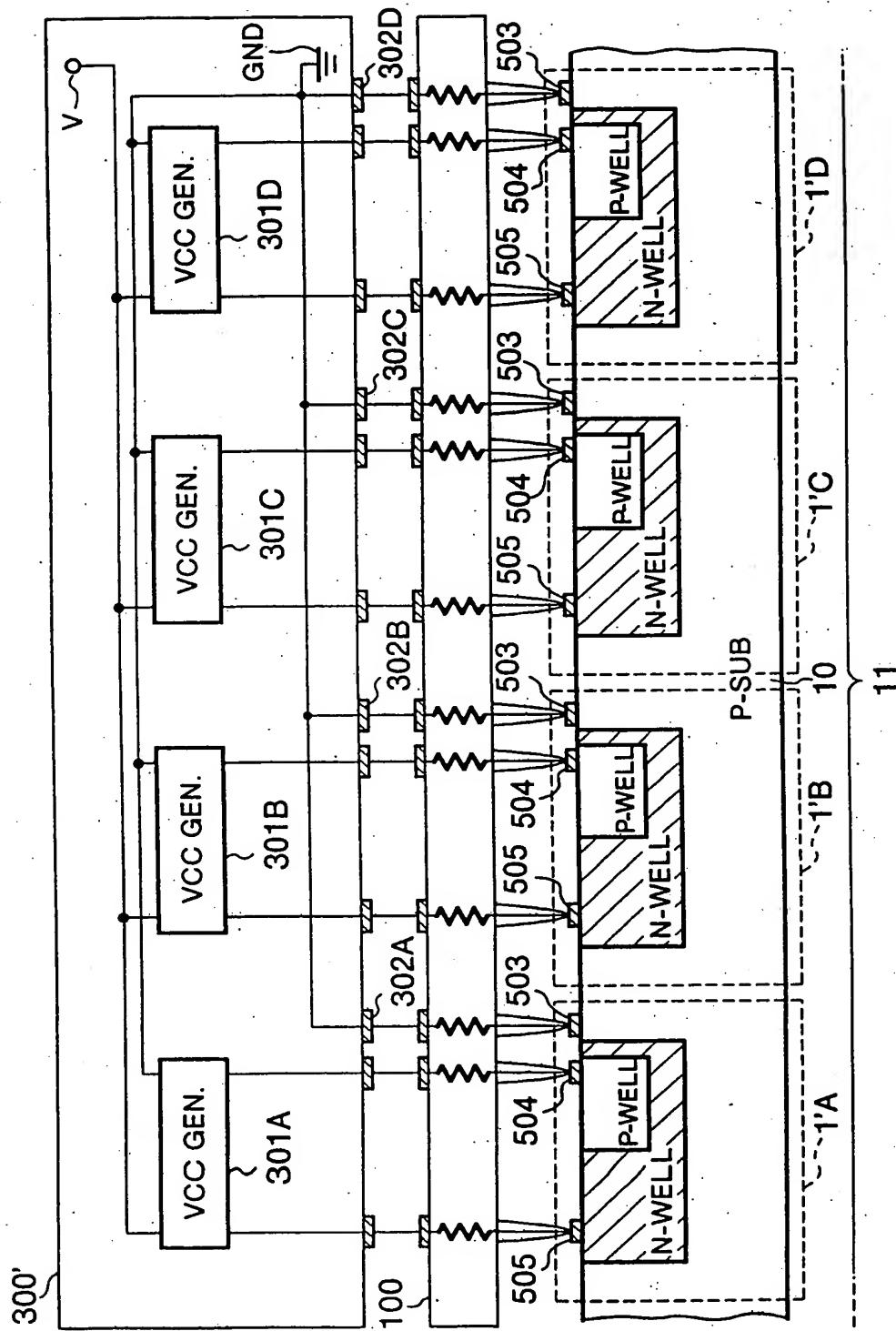


FIG. 35

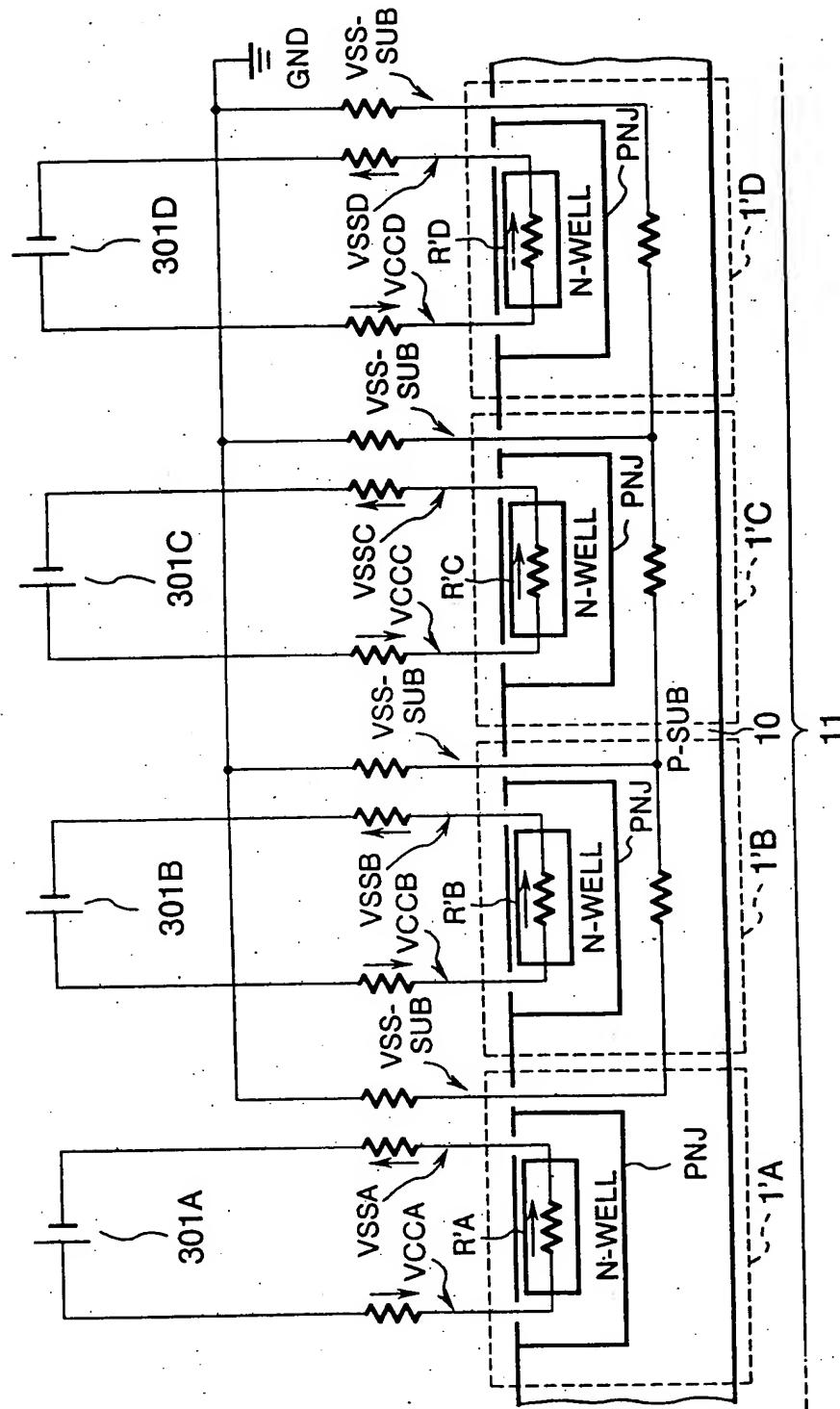


FIG. 36

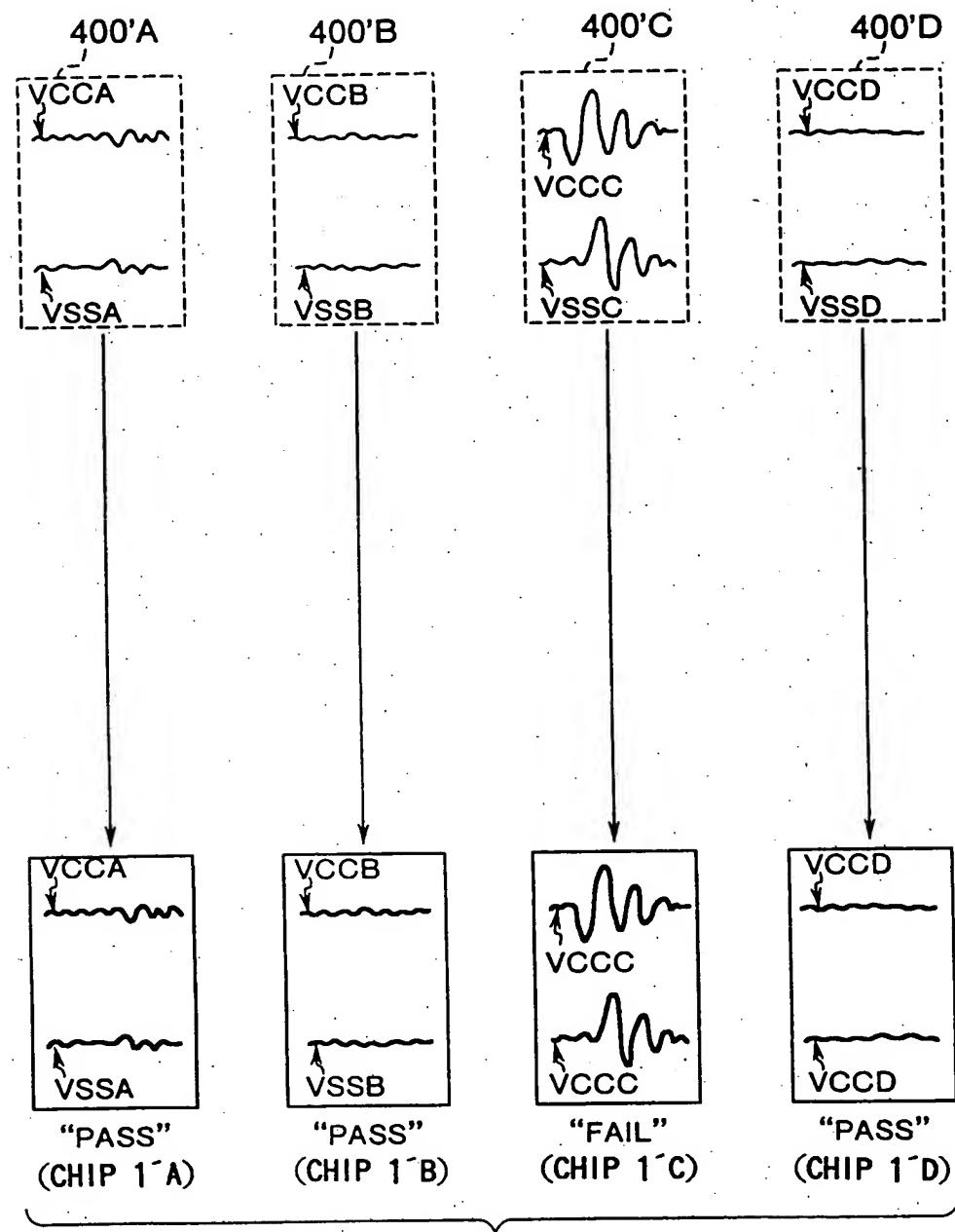


FIG. 37

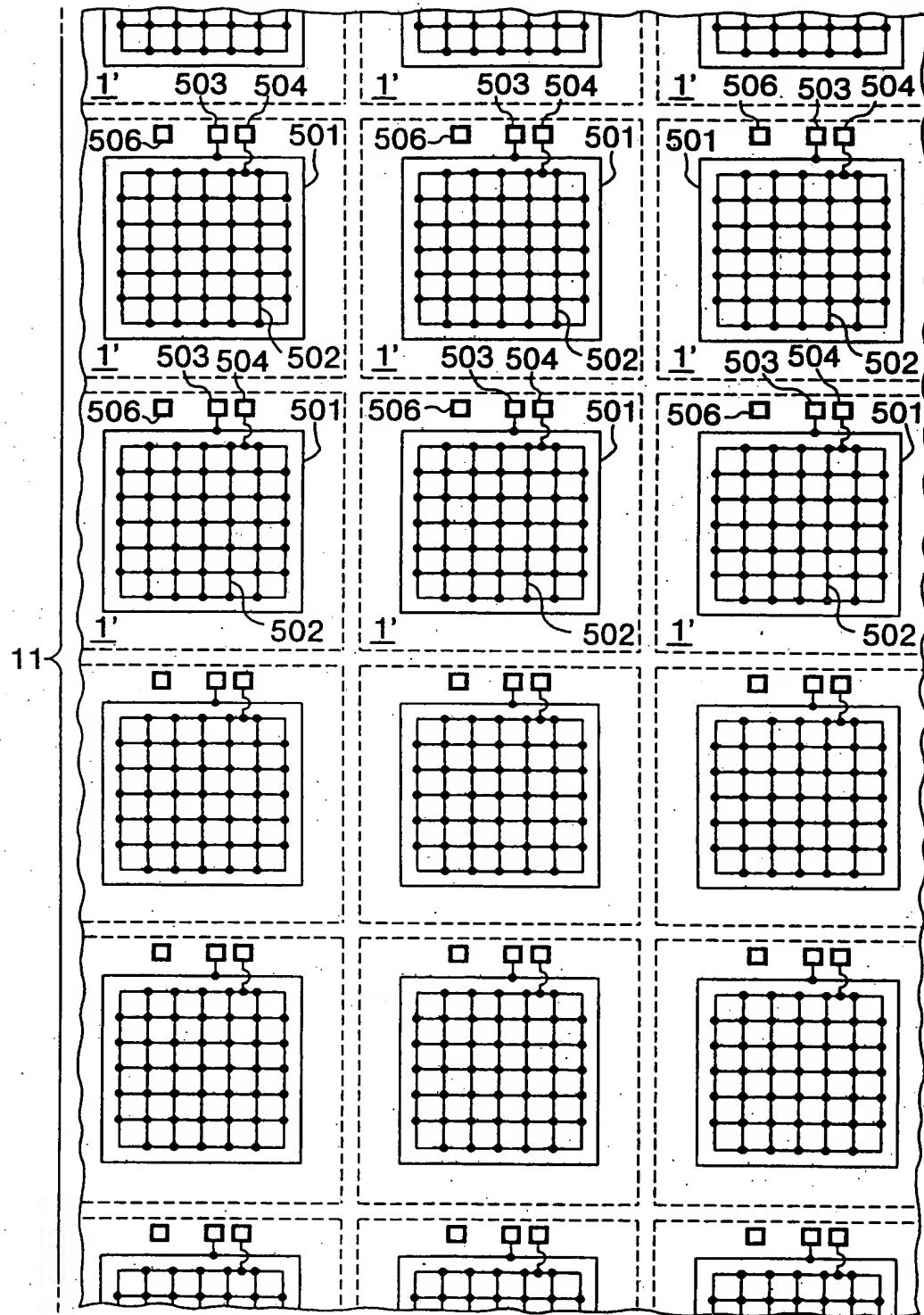


FIG. 38

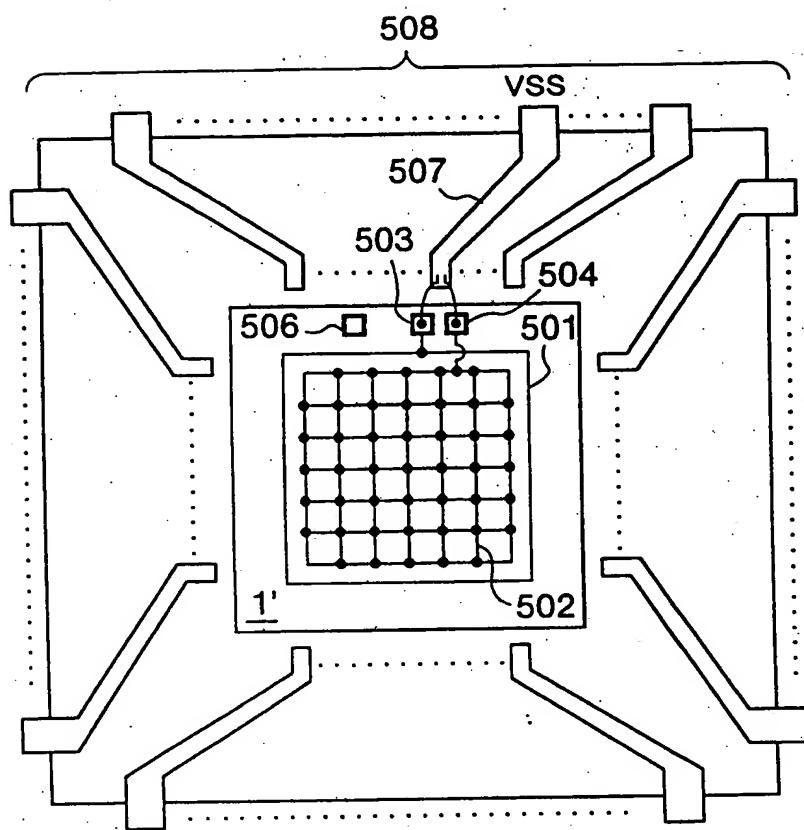


FIG. 39

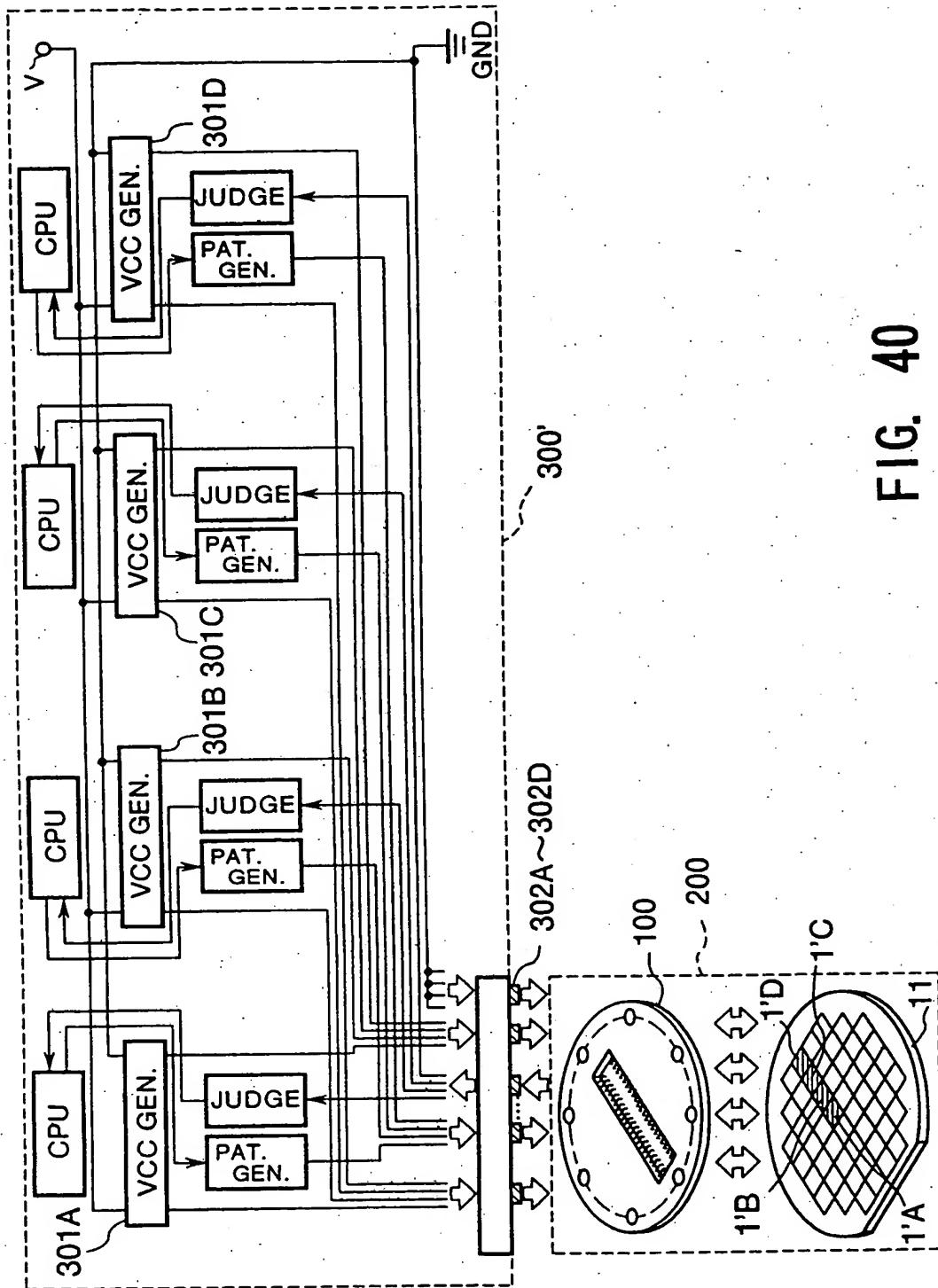


FIG. 40

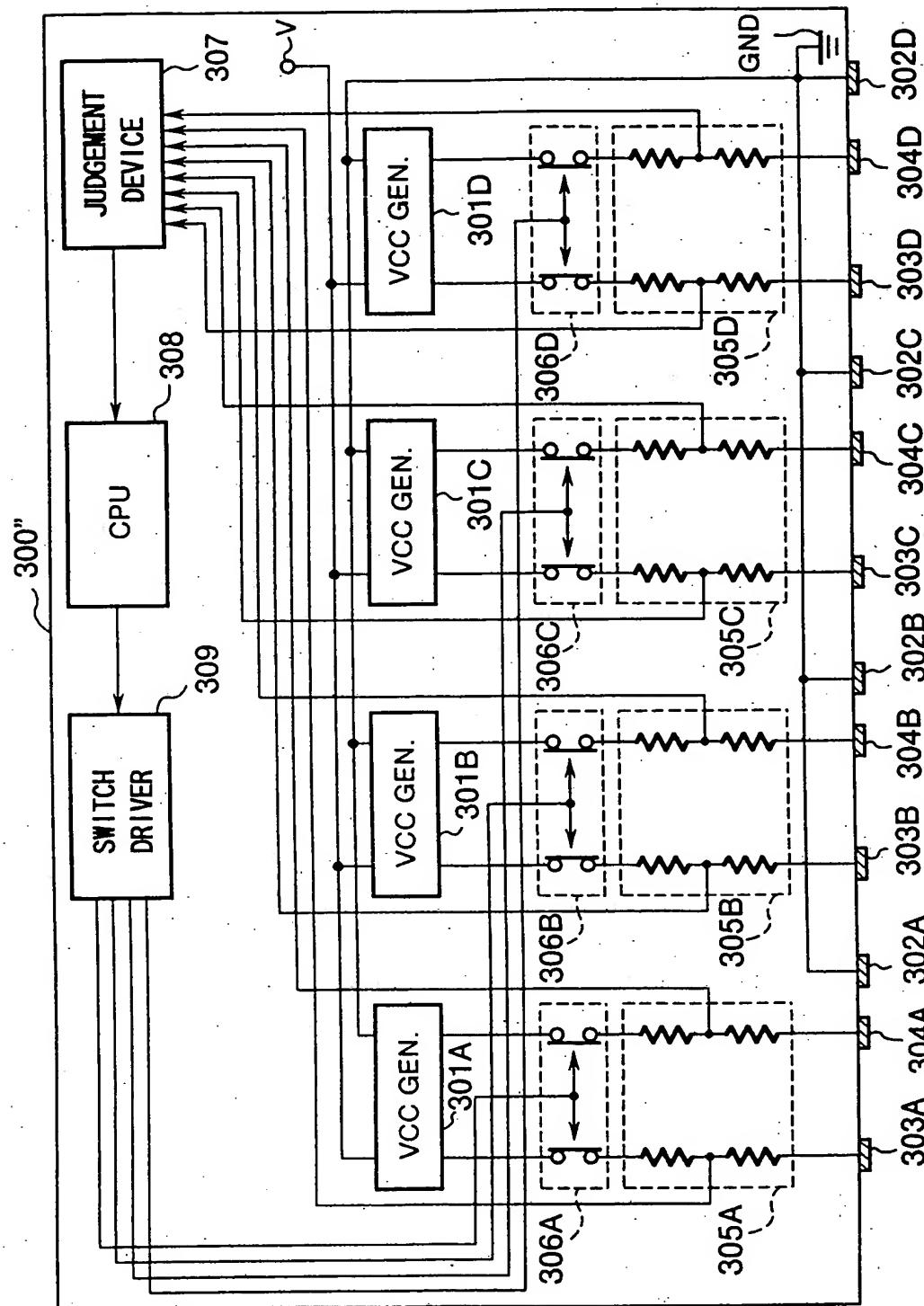


FIG. 41

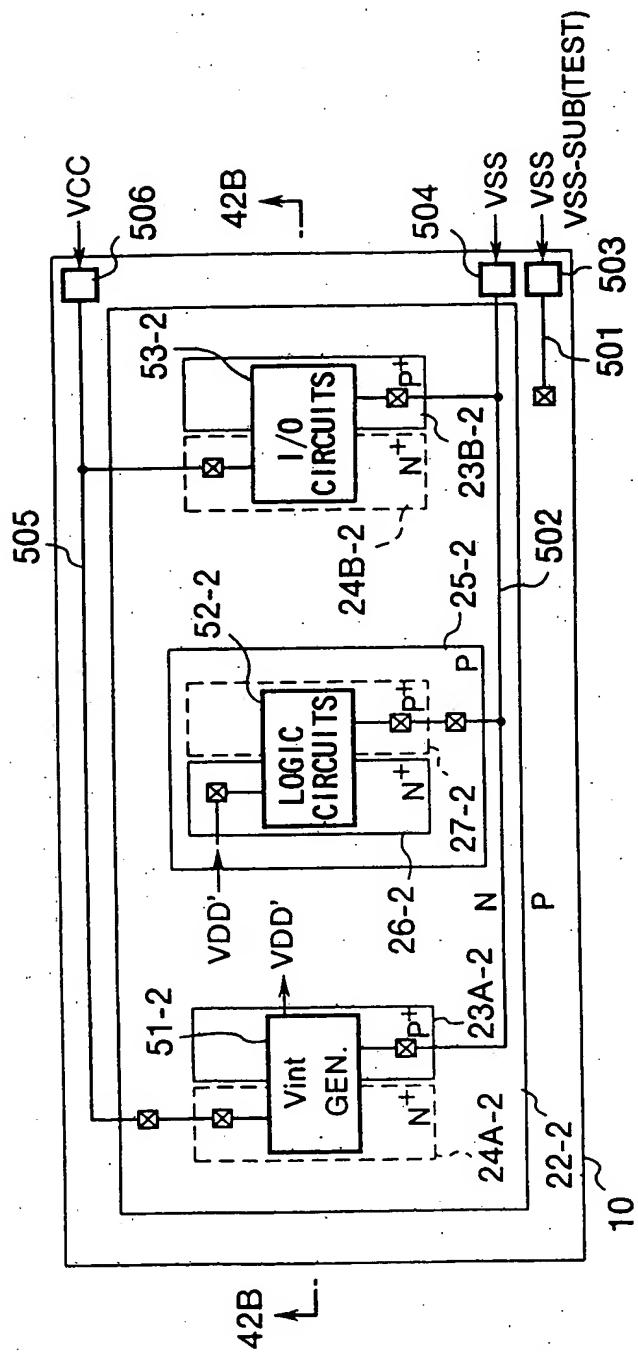
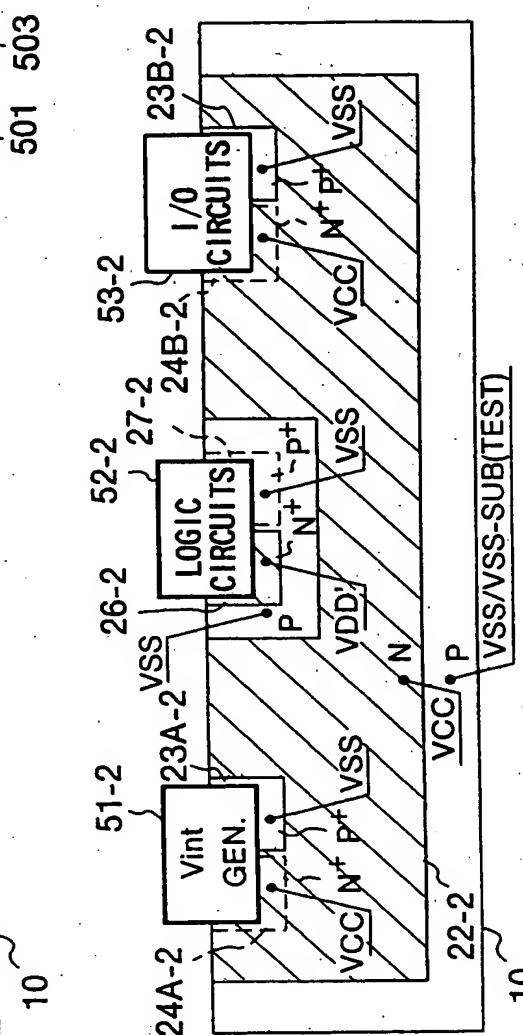


FIG. 42A



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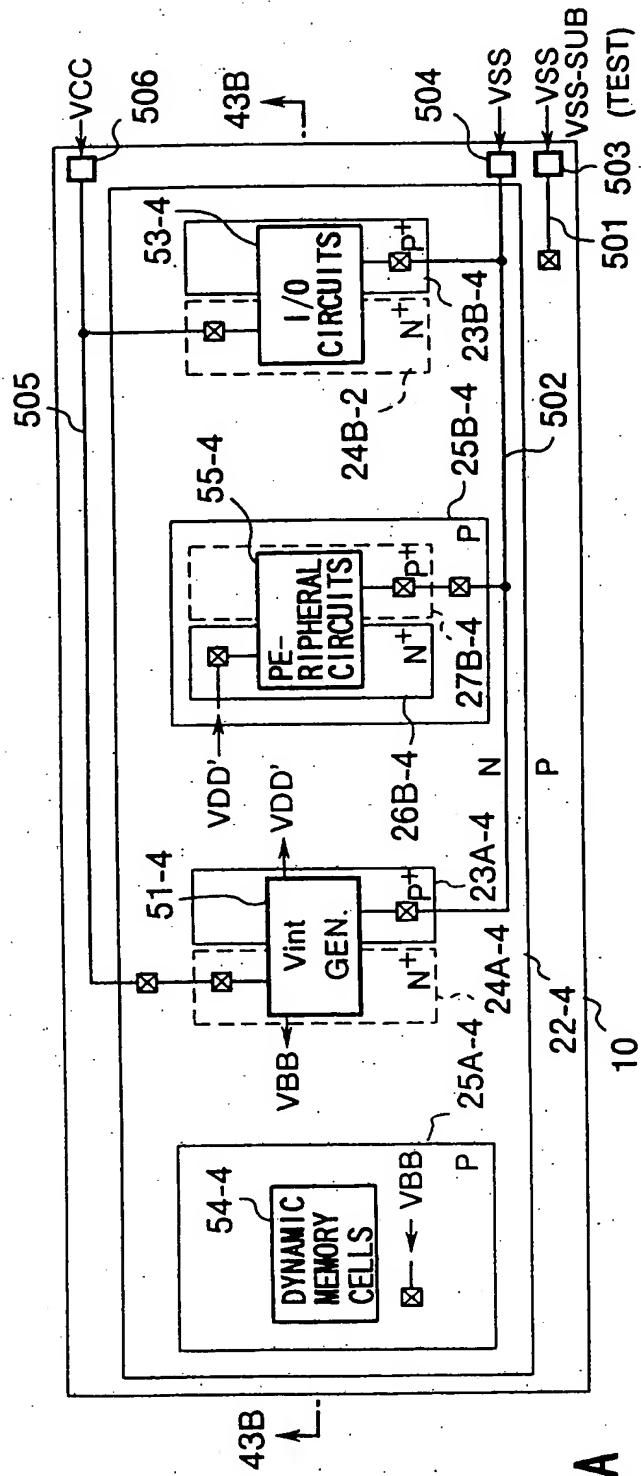


FIG. 43A

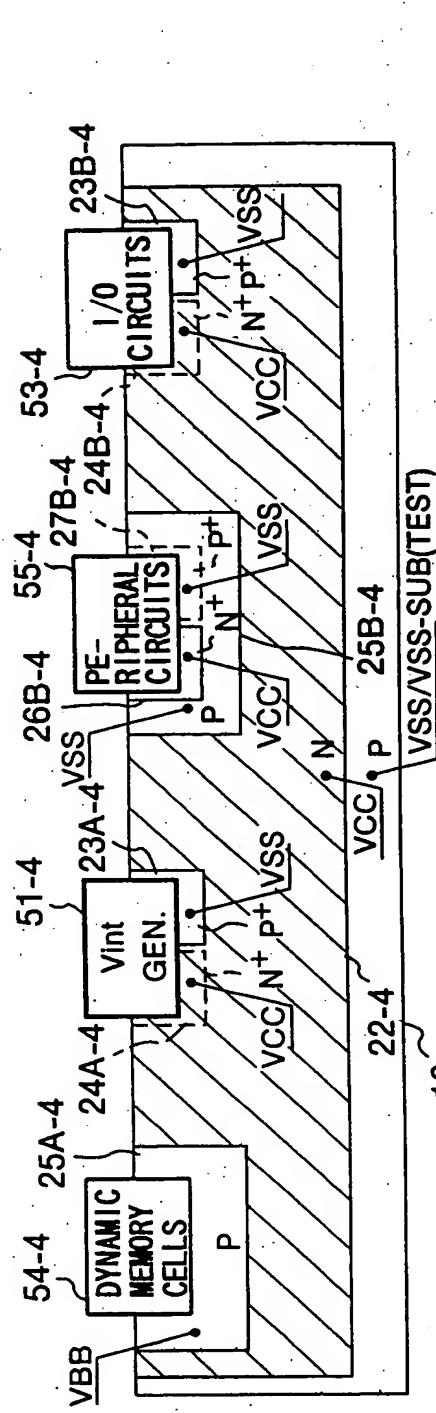


FIG. 43B

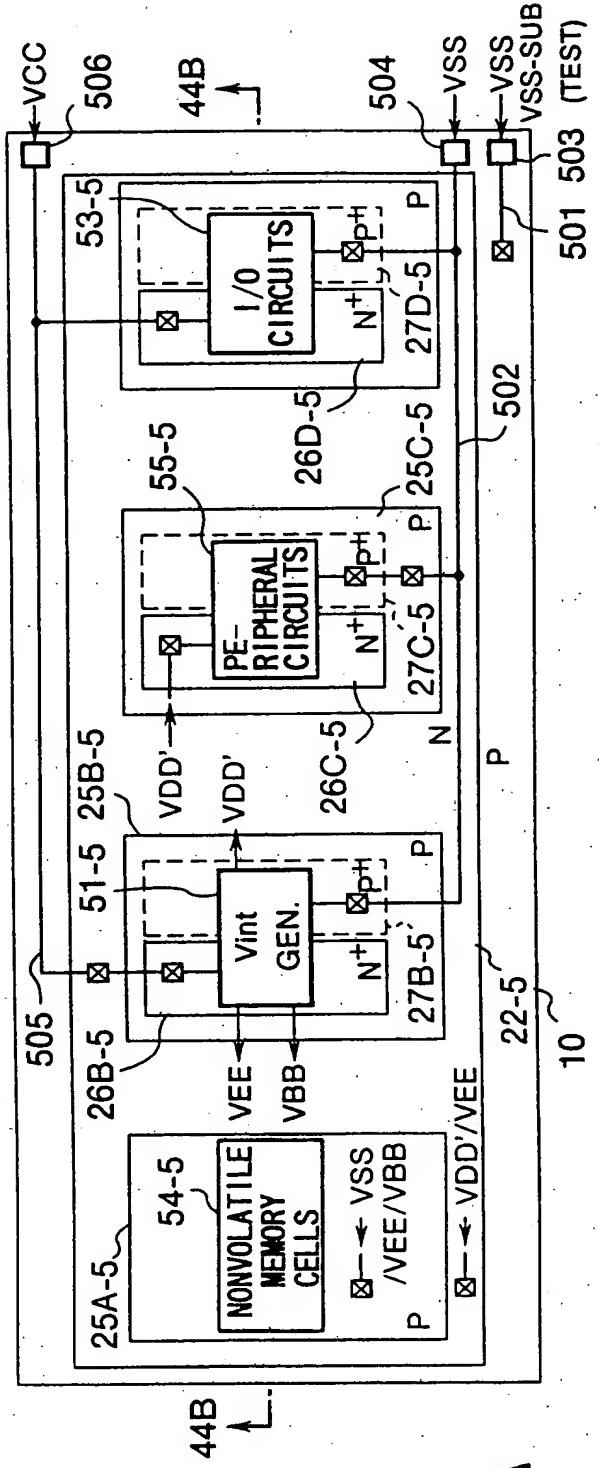


FIG. 44A

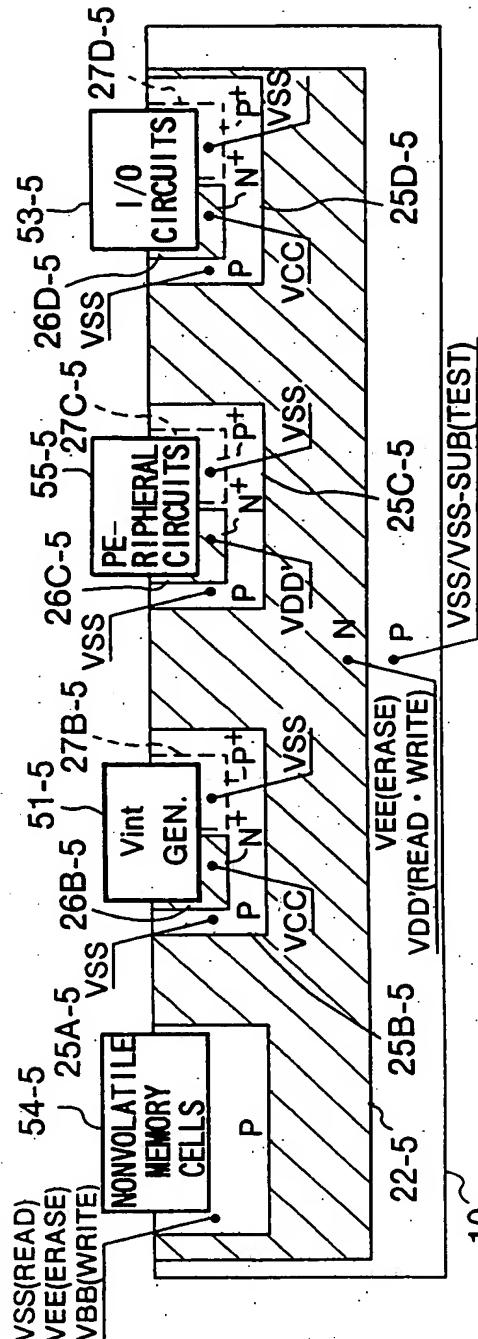


FIG. 44B

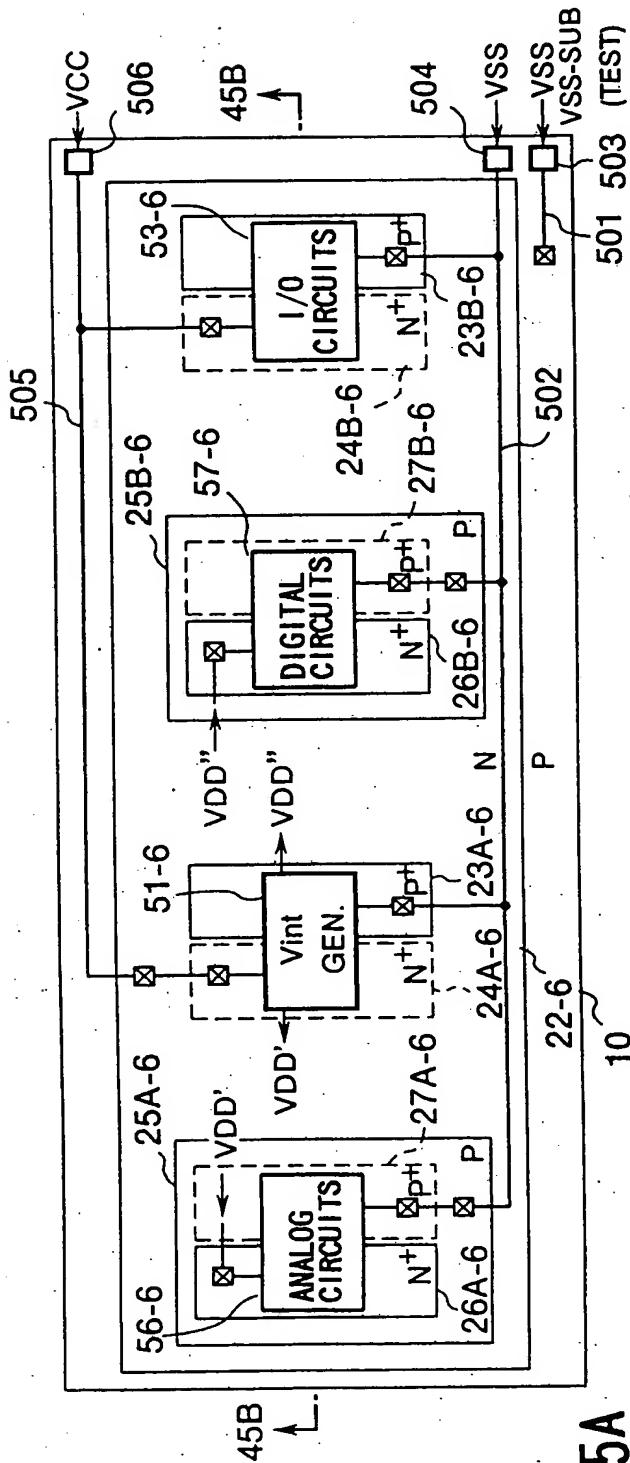


FIG. 45A

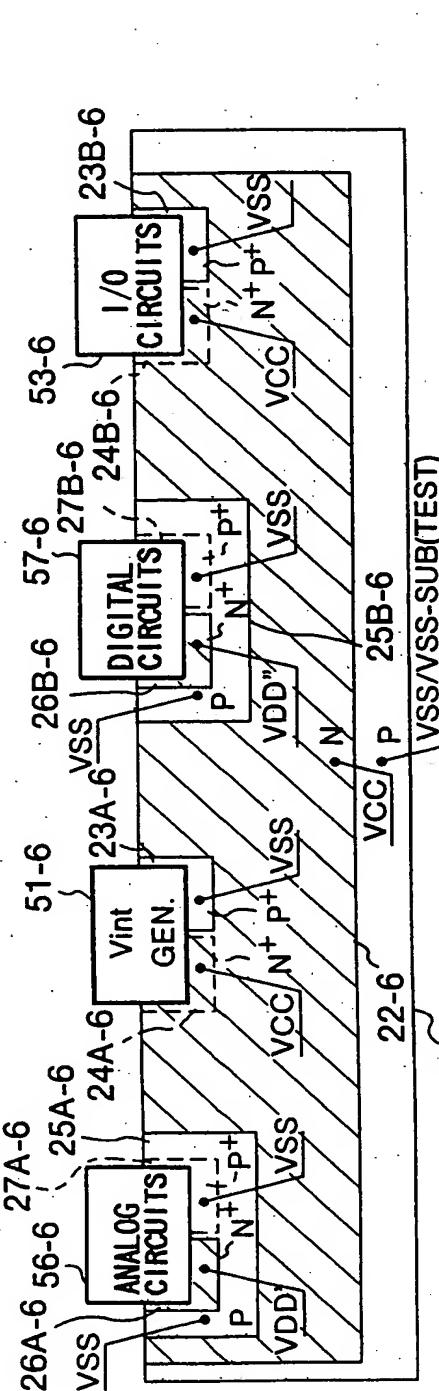
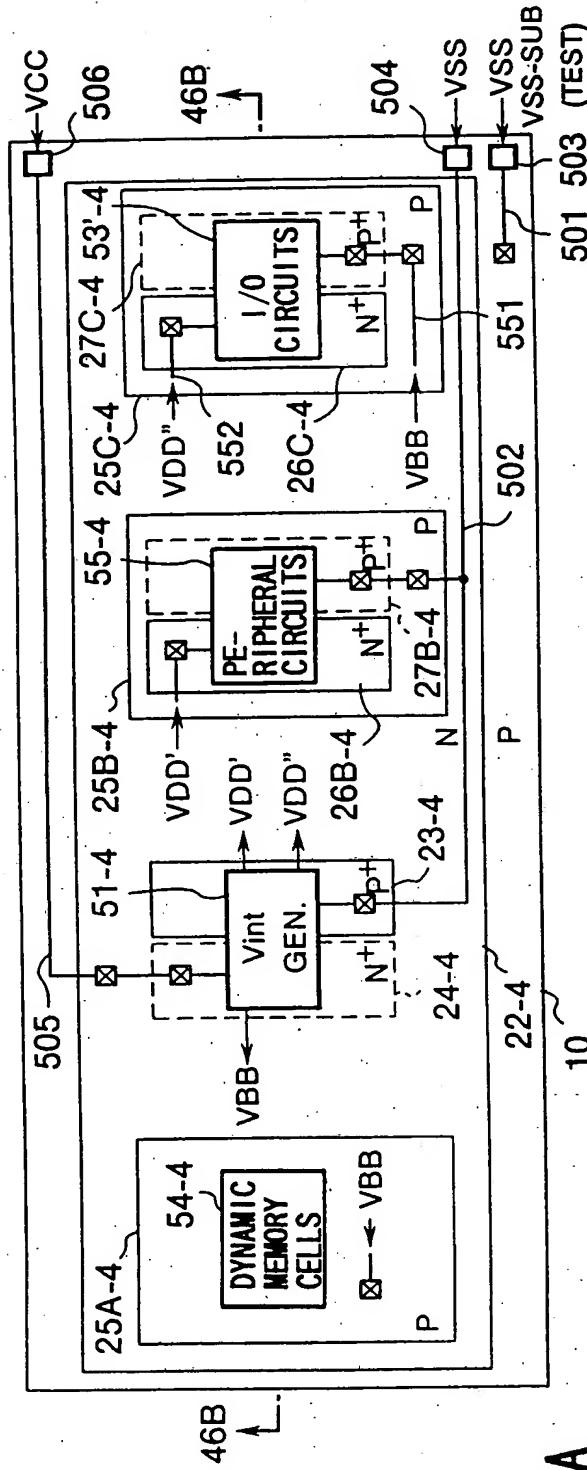
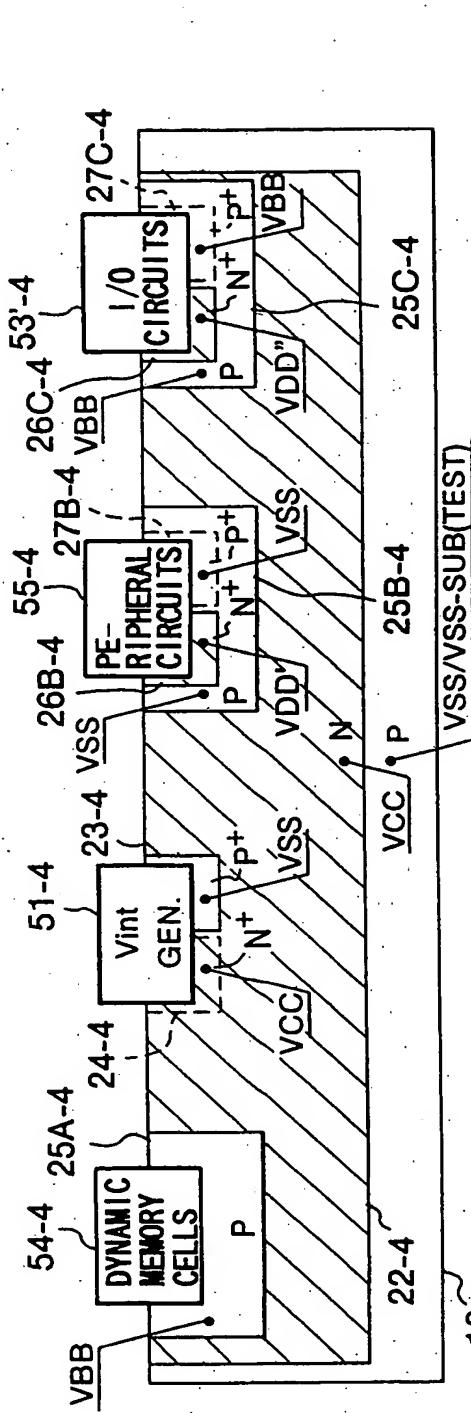


FIG. 45B



**FIG. 46A**



**FIG. 46B**

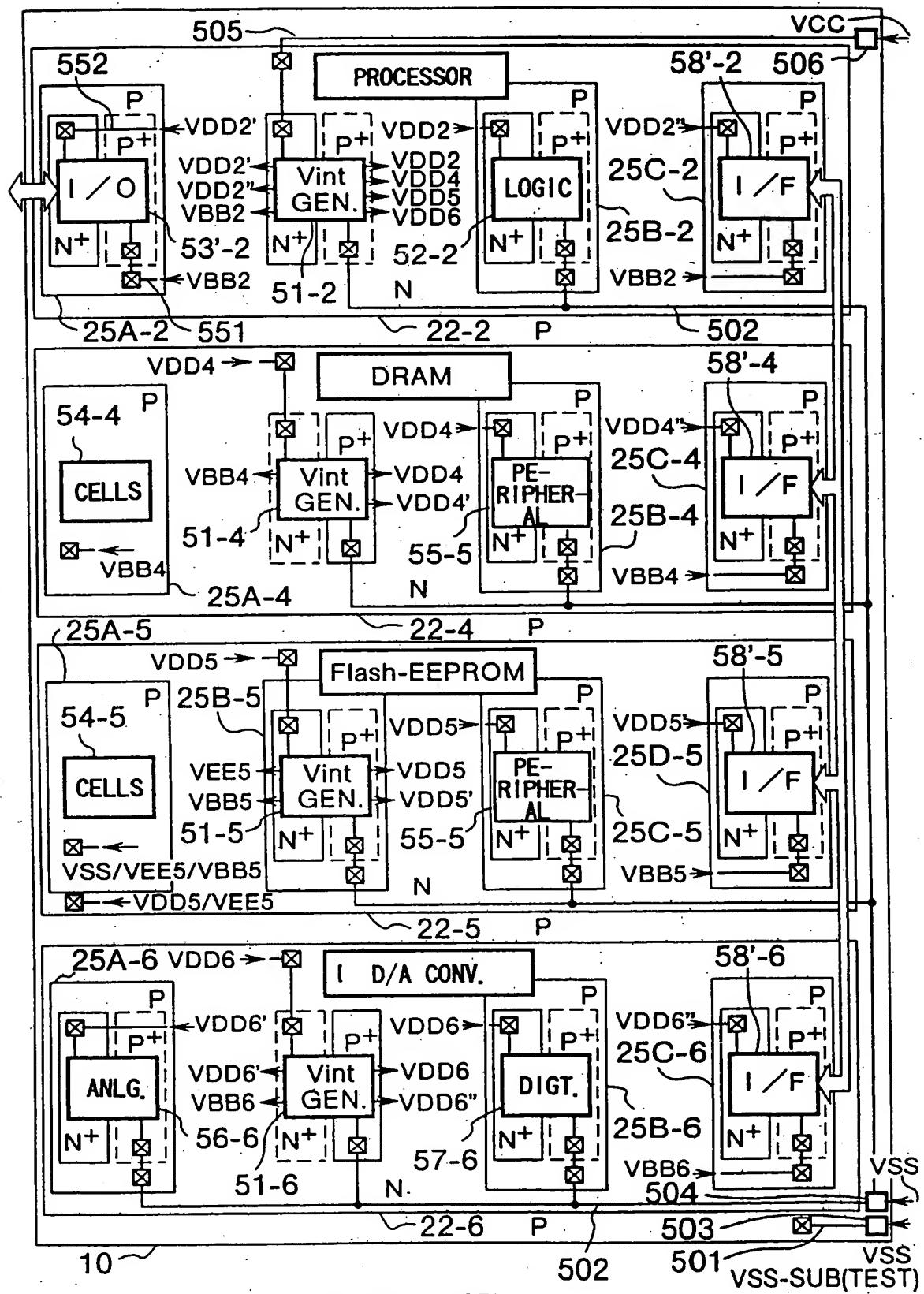


FIG. 47

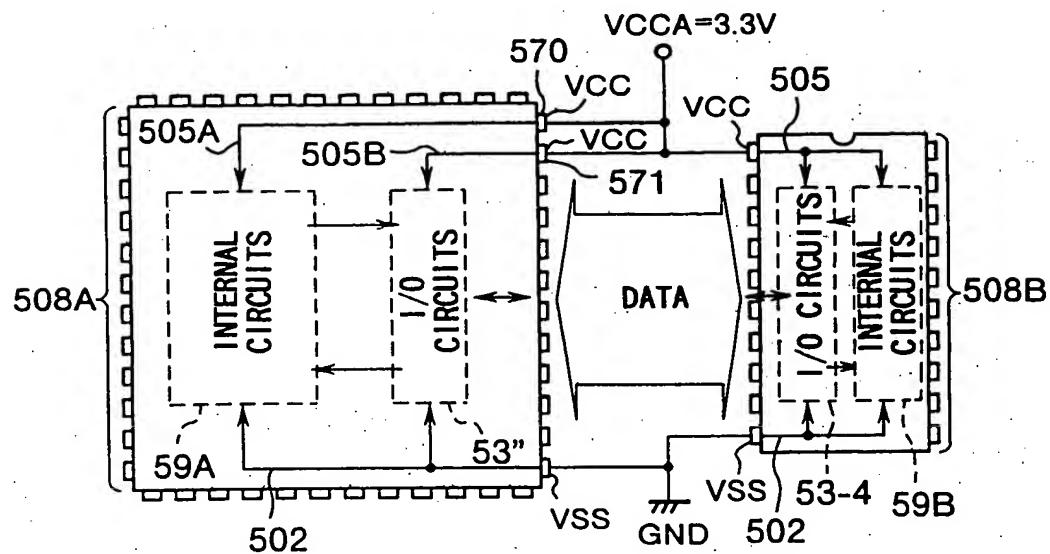


FIG. 48A

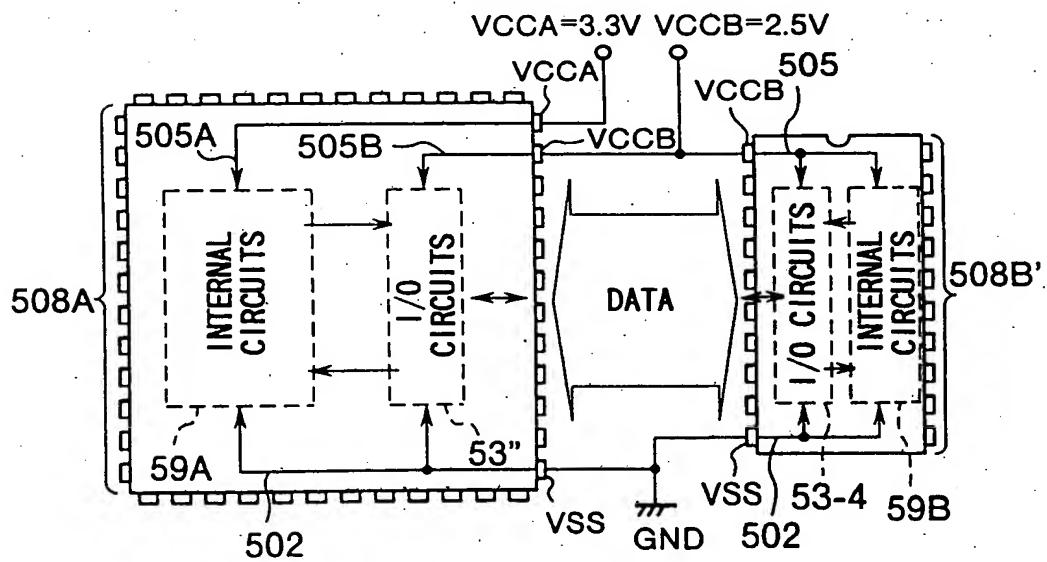


FIG. 48B

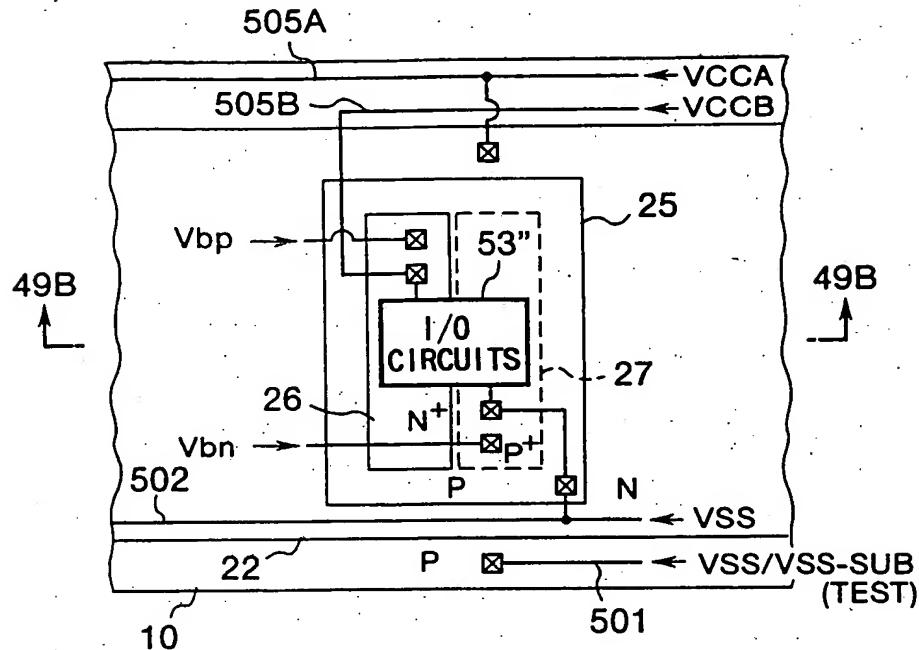


FIG. 49A

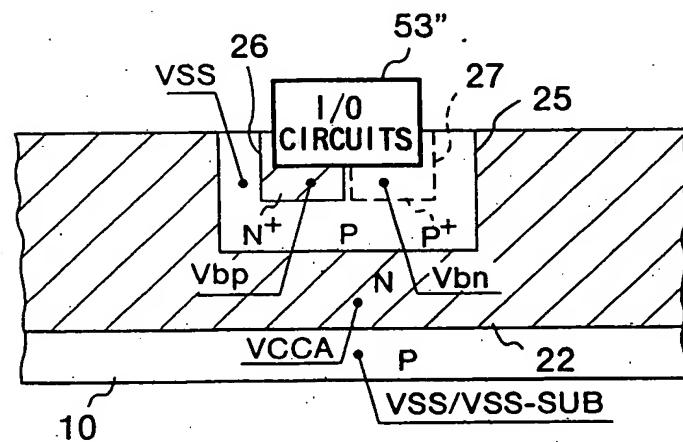


FIG. 49B

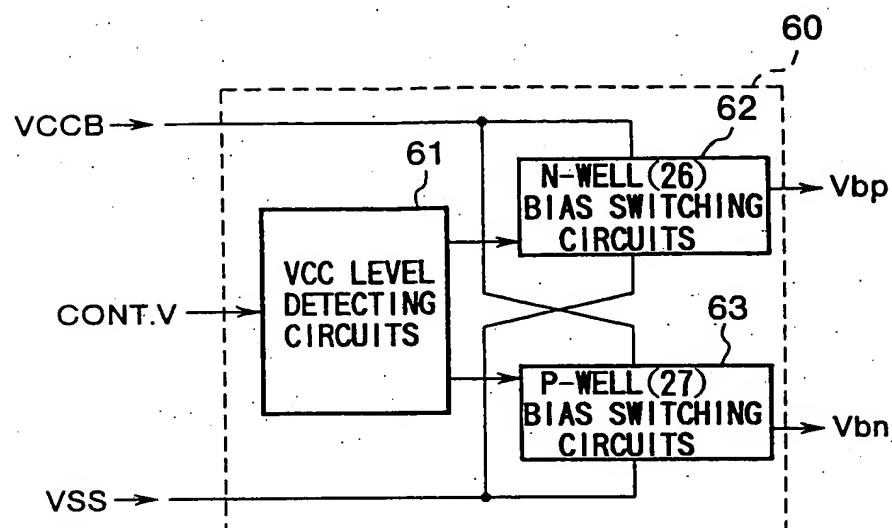


FIG. 50A

VCCA=3.3V VCCB=2.5V			VCCA = VCCB = 3.3V		
CONT.V	Vbp	Vbn	CONT.V	Vbp	Vbn
1	4.5V	-1.5V	0	3.3V (VCCB)	0V (VSS)

FIG. 50B

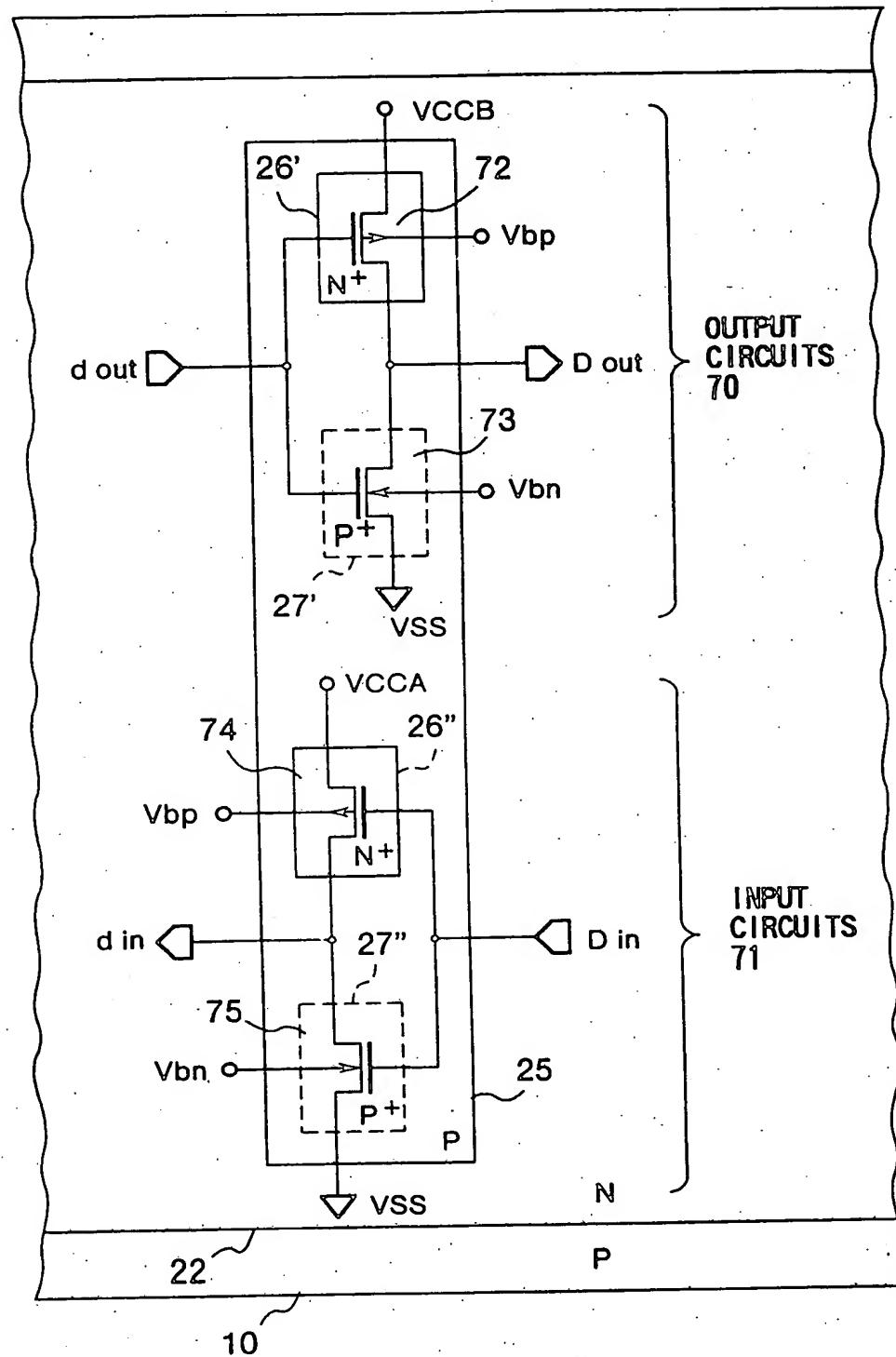


FIG. 51

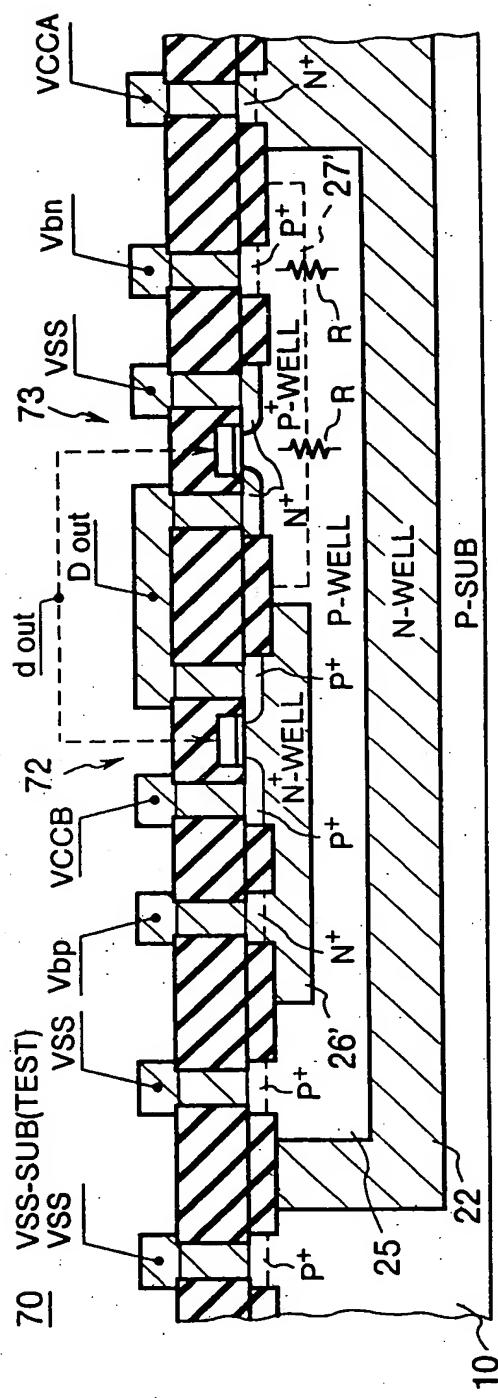


FIG. 52A

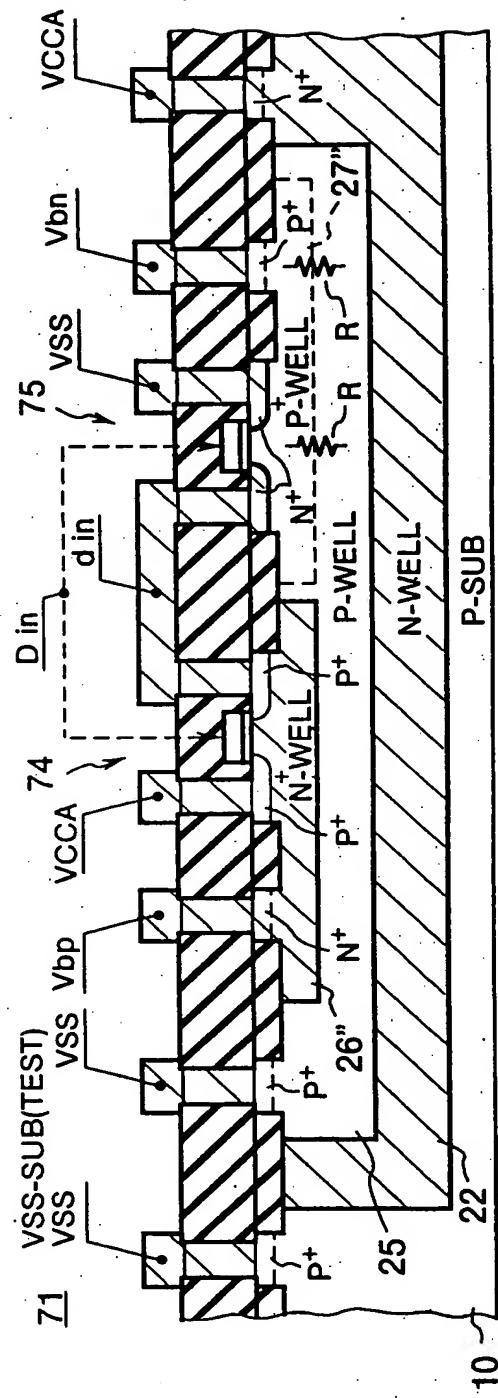


FIG. 52B

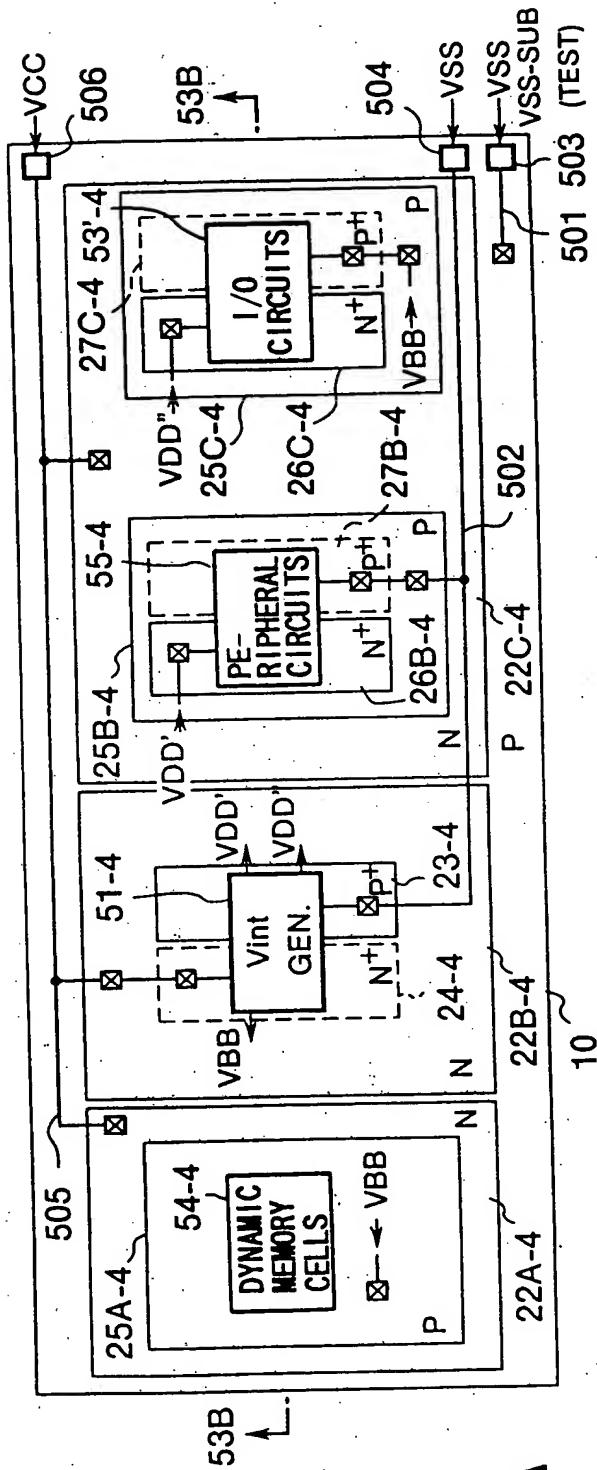
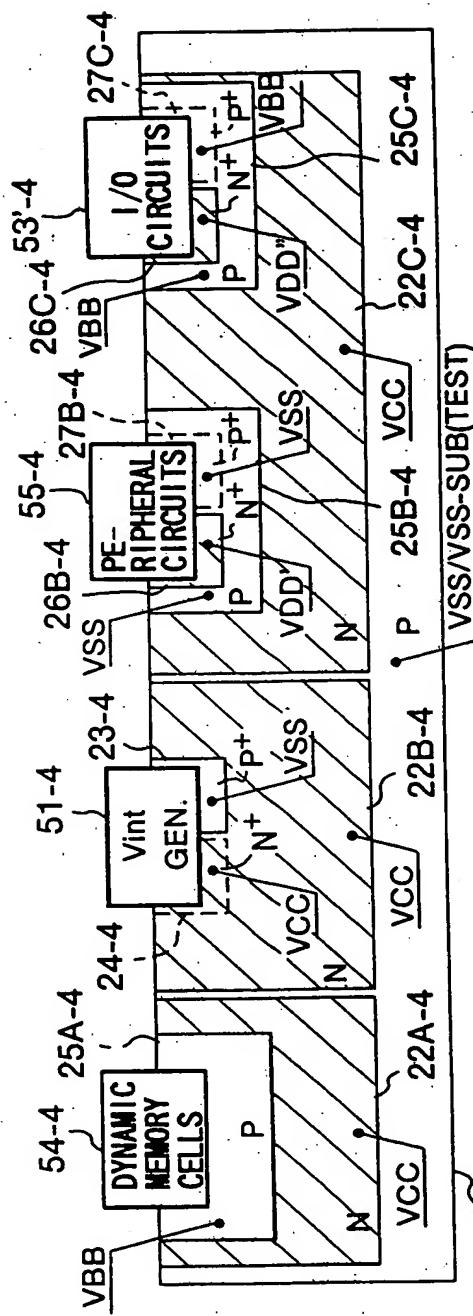


FIG. 53A



VSS/VSS-SUB(TEST) 10

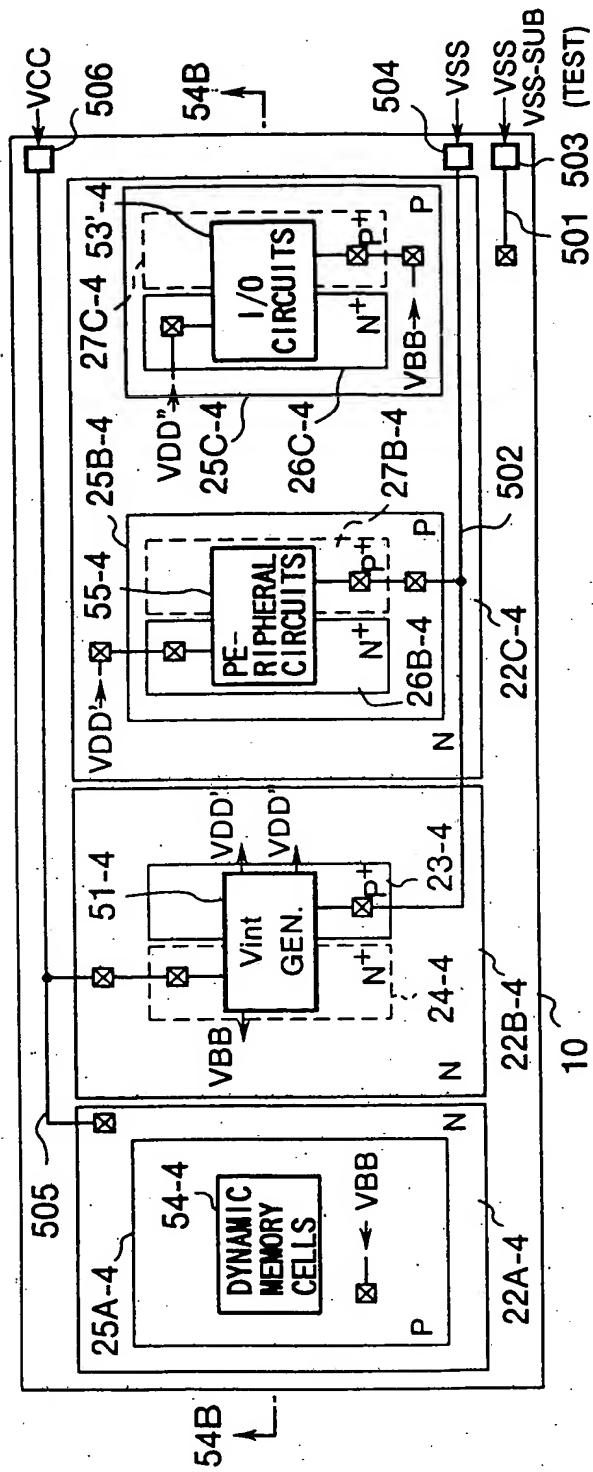


FIG. 54A

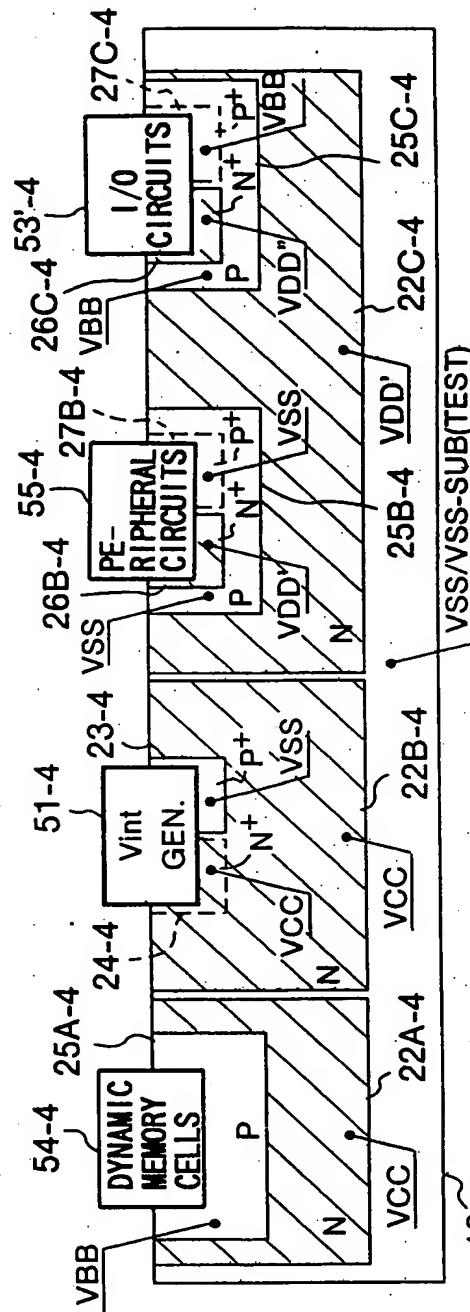


FIG. 54B

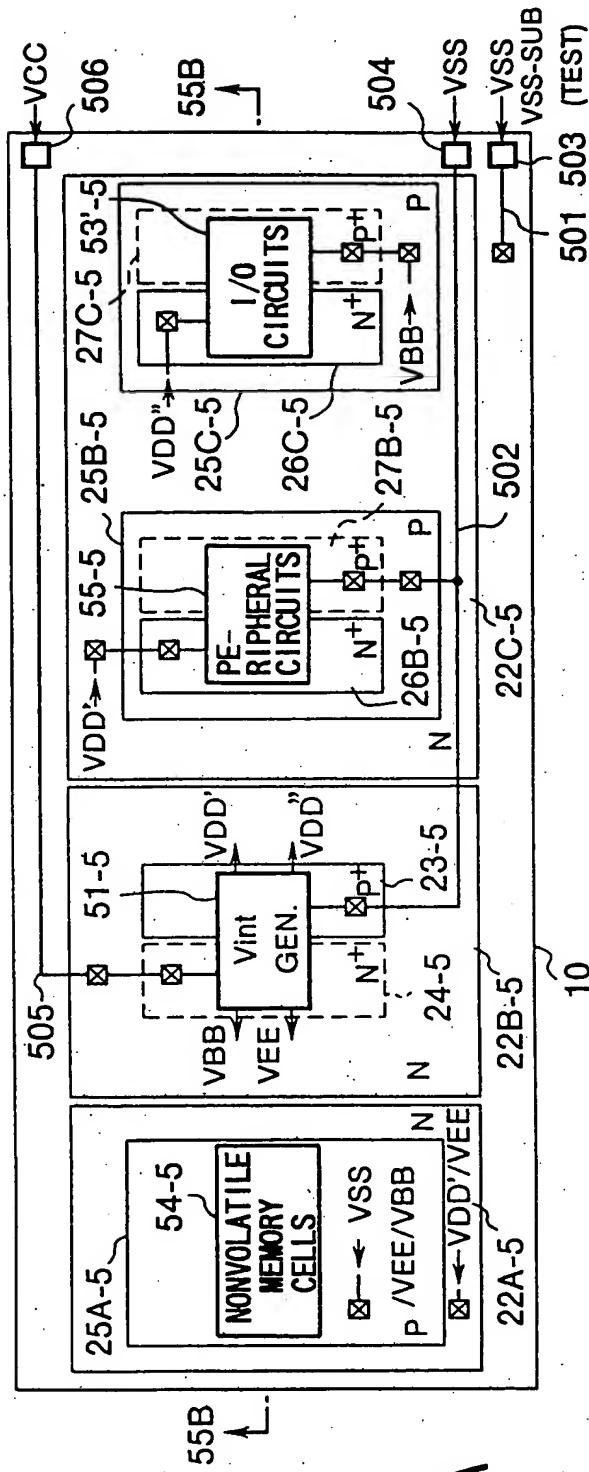
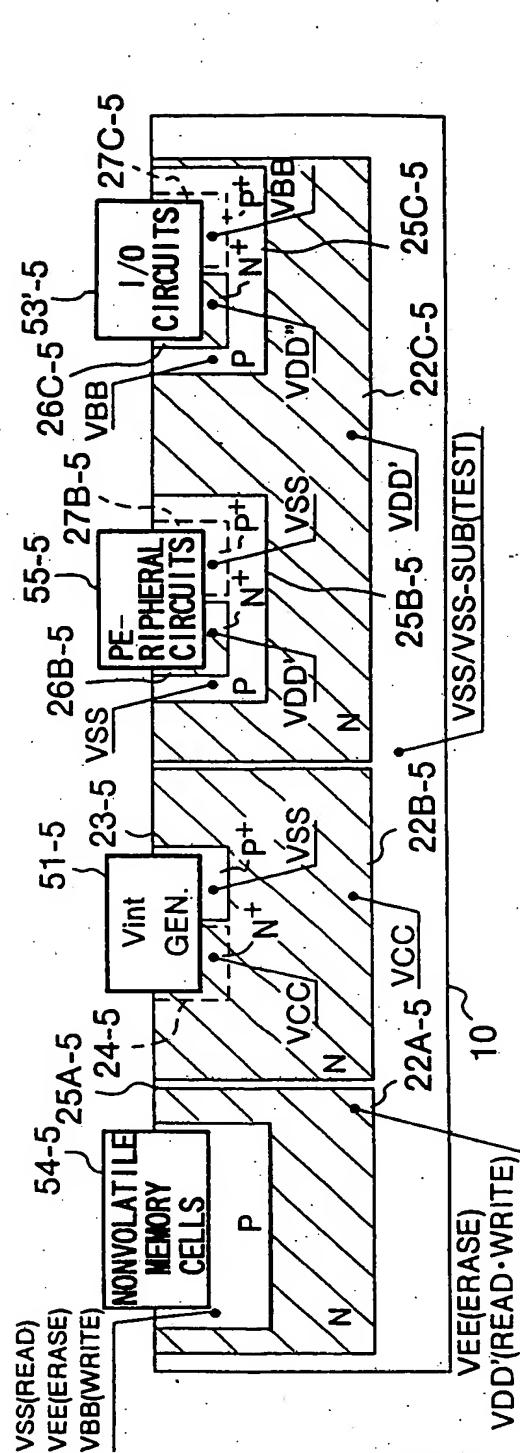
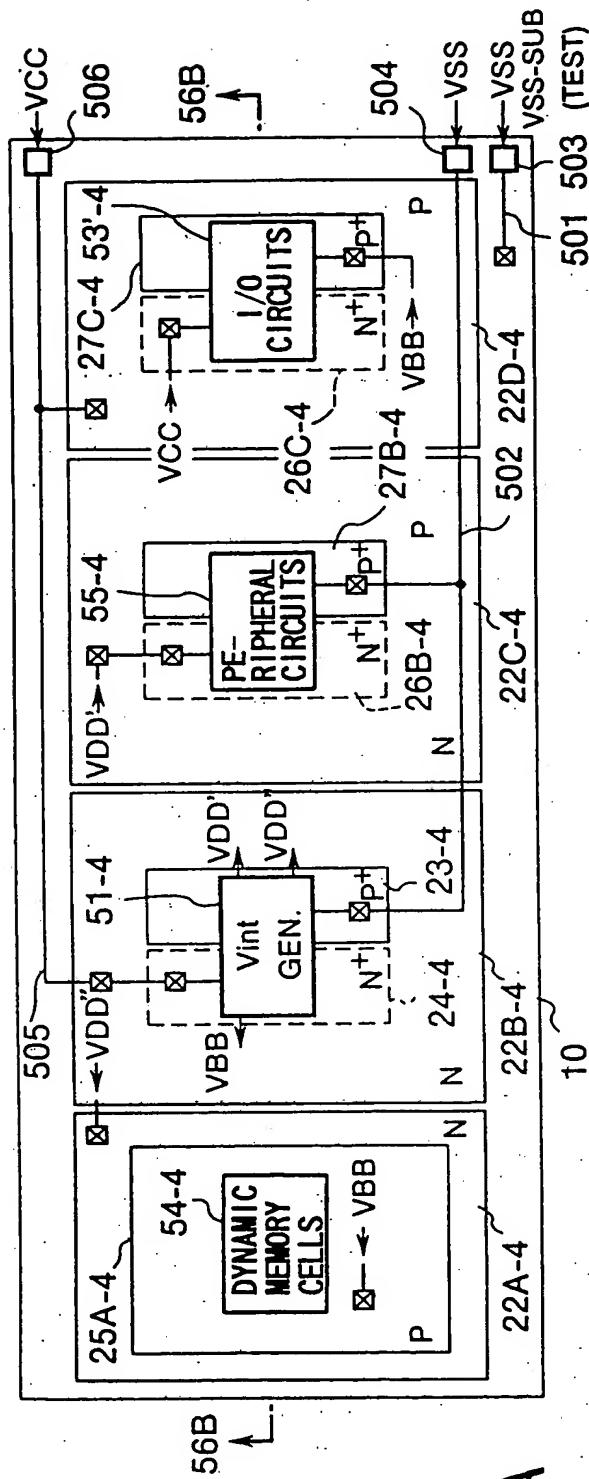


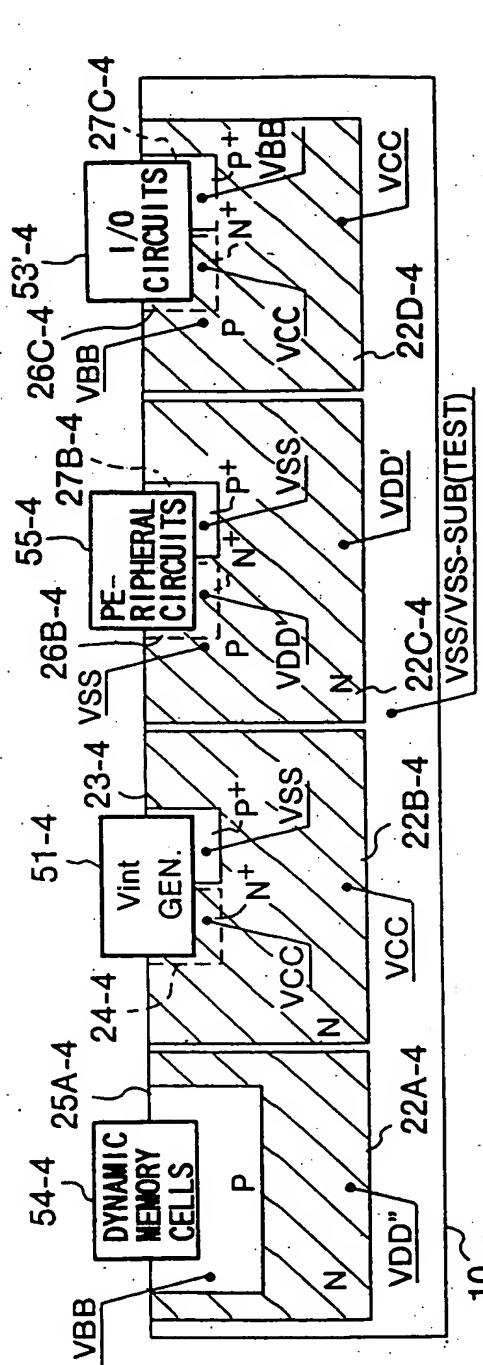
FIG. 55A



**FIG.** 55B



**FIG. 56A**



**FIG. 56B**

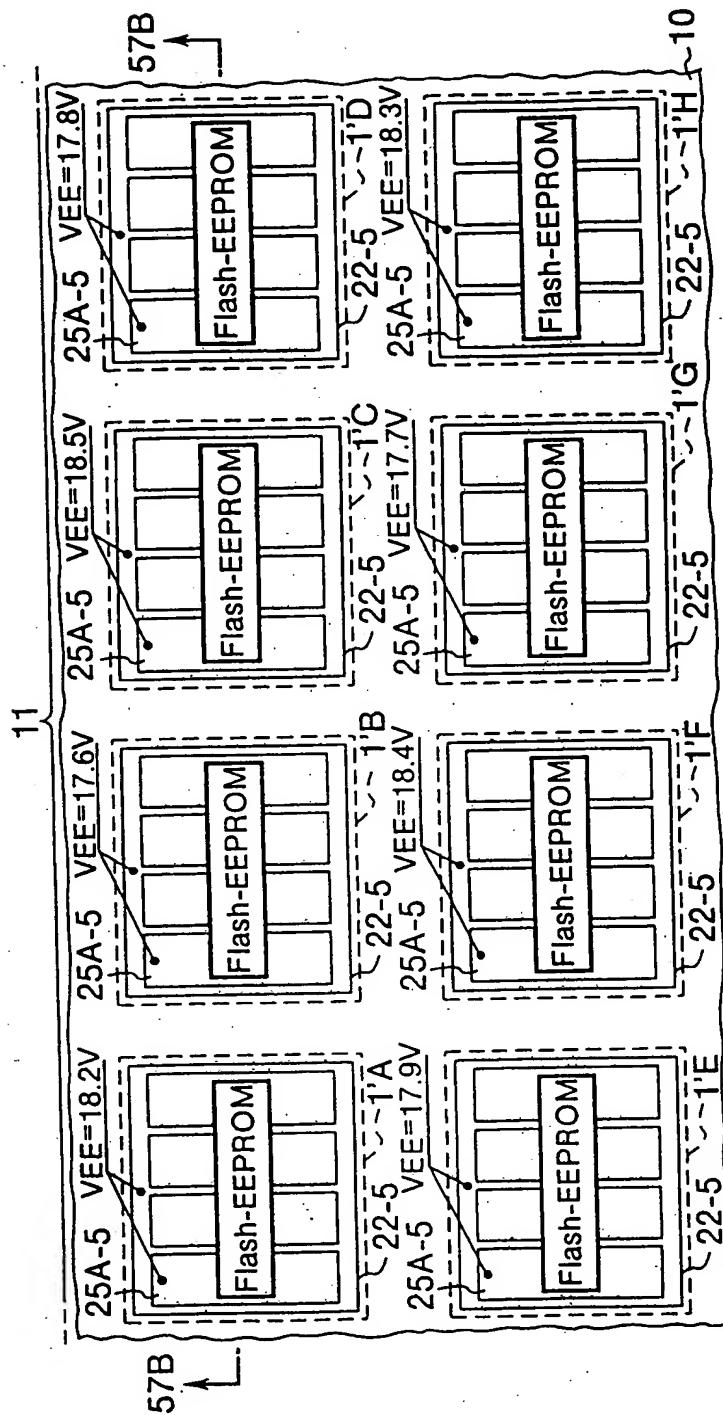


FIG. 57A

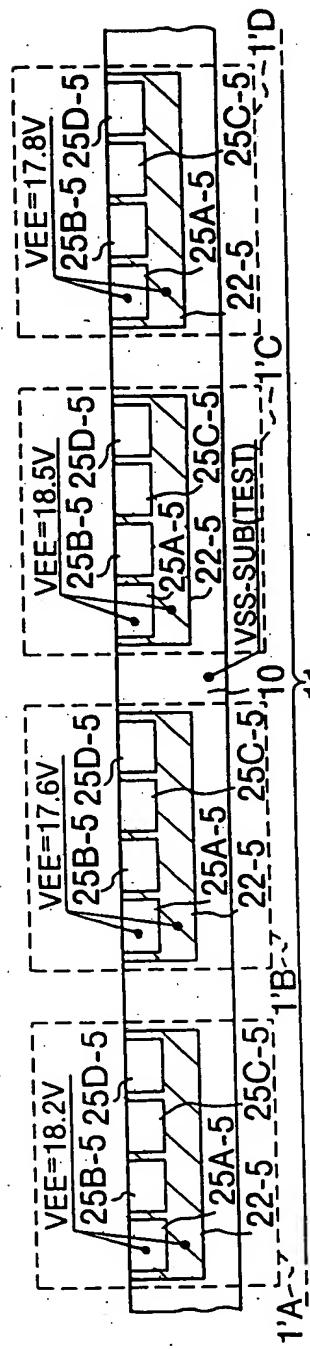


FIG. 57B